PC-HELPER

High-Resolution Analog Input Board
for Low Profile PCI

AD16-64(LPCI)LA

User's Guide

CONTEC CO., LTD.

Check Your Package

Thank you for purchasing the CONTEC product.

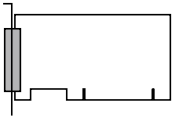
The product consists of the items listed below.

Check, with the following list, that your package is complete. If you discover damaged or missing items, contact your retailer.

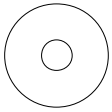
Product Configuration List

- Board [AD16-64(LPCI)LA] …1
- First step guide … 1
- Disk *1 [API-PAC(W32)] …1
- Standard-sized bracket…1
- Warranty Certificate …1
- Serial Number Label …1

*1 The bundled disk contains the driver software and User's Guide (this guide)



Board



Disk
[API-PAC(W32)]

Standard size bracket

First step guide

Warranty Certificate

Serial Number Label

Copyright

Trademarks

Table of Contents

1. Before Using the Product

About the Board

The < AD16-64(LPCI)LA > is a PCI bus compatible interface board with 64 high-precision, 16-bit analog input channels, digital input and output channels (4 channels each), and a 32-bit counter (1 channel)

This product supports a Low Profile size slot and, if replaced with the supplied bracket, supports a standard size slot, too. The board can build a space-saving PC into a cost-effective analog input system.

Using the bundled API function library package [API-PAC(W32)], you can create Windows application software for this board in your favorite programming language supporting Win32 API functions, such as Visual Basic or Visual C++.

It can also collect data easily without a program when the data logger software [C-LOGGER] stored on the attached disk is used. With plug-ins for the dedicated libraries, the board also supports MATLAB and LabVIEW.

Features

Multi channel analog input

Capable of analog input of 64 single-ended input channels or 32 differential input channels.
The selection of the single-ended input and the difference input can be set with software.

Rich set of basic functions

Compact system providing high-precision analog inputs.
Equipped with analog inputs, analog input control signals (TTL level, 3ch), digital inputs (TTL level, 4ch), digital outputs (TTL level, 4ch), and counter (TTL level 32-bit, 1ch)

Substantial control functions

Capable of analog input either time-based mode or external-signal synchronous mode.

Filter function facilitating external signal connection

The external control signals for analog input incorporate a digital filter to prevent problems such as chattering.

Software-based calibration function

Calibration of analog input can be all performed by software. Apart from the adjustment information prepared before shipment, additional adjustment information can be stored according to the use environment.

Exchangeable Low Profile size and standard size slots

Support for both of Low Profile size and standard size slots (interchangeable with a bundled bracket).

Supported to the data logger software [C-LOGGER]

Supporting the data logger software [C-LOGGER] that enables the graph display of recorded signal data, file saving, and dynamic transfer to the spreadsheet software program “Excel”

Plug-ins for the dedicated libraries, the board also supports MATLAB and LabVIEW.

We offer a dedicated library [ML-DAQ], which allows you to use this product on MATLAB by the MathWorks as well as another dedicated library [VI-DAQ], which allows you to use the product on LabVIEW.
These dedicated libraries are available, free of charge (downloadable), on our web site.

Support Software

You should use CONTEC support software according to your purpose and development environment.

Windows version of analog I/O driver **API-AIO(WDM)**
[Stored on the bundled disk driver library API-PAC(W32)]

The API-AIO(WDM) is the Windows version driver library software that provides products in the form of Win32 API functions (DLL). Various sample programs such as Visual Basic and Visual C++, etc and diagnostic program *1useful for checking operation is provided.

For more details on the supported OS, applicable language and new information, please visit the CONTEC's Web site.

Linux version of analog I/O driver **API-AIO(LNX)**
[Stored on the bundled disk driver library API-PAC(W32)]

The API-AIO(LNX) is the Linux version driver software which provides device drivers (modules) by shared library and kernel version. Various sample programs of gcc are provided.

For more details on the supported OS, applicable language and new information, please visit the CONTEC's Web site.

Data Logger Software **C-LOGGER**
[Stored on the bundled disk driver library API-PAC(W32)]

C-LOGGER is a data logger software program compatible with our analog I/O products. This program enables the graph display of recorded signal data, zoom observation, file saving, and dynamic transfer to the spreadsheet software "Excel". No troublesome programming is required.

For more details on the supported OS, applicable language and new information, please visit the CONTEC's Web site.

Data Acquisition library for MATLAB **ML-DAQ**
(Available for downloading (free of charge) from the CONTEC web site.)

This is the library software which allows you to use our analog I/O device products on MATLAB by the MathWorks. Each function is offered in accordance with the interface which is integrated in MATLAB's Data Acquisition Toolbox.
For more details on the supported OS, applicable language and new information, please visit the CONTEC's Web site.

Data acquisition VI library for LabVIEW **VI-DAQ**
(Available for downloading (free of charge) from the CONTEC web site.)

This is a VI library to use in National Instruments LabVIEW.
VI-DAQ is created with a function form similar to that of LabVIEW's Data Acquisition VI, allowing you to use various devices without complicated settings.
For more details on the library and download of VI-DAQ, please visit the CONTEC's Web site.

Cable & Connector (Option)

Shielded cables with two-ended connector for 68-pin half-pitch connector

: PCB68PS-0.5P (0.5m)

: PCB68PS-1.5P (1.5m)

Shielded cables with single-ended connector for 68-pin half-pitch connector

: PCA68PS-0.5P (0.5m)

: PCA68PS-1.5P (1.5m)

68/96-pin conversion shielded cable for analog input/output

: ADC-68M/96F (0.5m)

* Two sets of cables are required to use both connector CNA and CNB.

Accessories (Option)

Screw Terminal (M2.5 x 96P)	: DTP-64(PC) *1*3
Screw Terminal (M3 x 68P)	: EPD-68A *2*3*4
Screw Terminal (M3 x 96P)	: EPD-96A *1*3*4
Screw Terminal (M3.5 x 96P)	: EPD-96 *1*3
BNC Terminal Unit (analog input 32ch)	: ATP-32F *1*3
BNC Terminal Unit (analog input 8ch)	: ATP-8 *1*3*5

*1 ADC-68M/96F optional cable is required separately.
*2 PCB68PS-0.5P or PCB68PS-1.5P optional cable is required separately.
*3 Two sets of cables are required to use both connector CNA and CNB.
*4 “Spring-up” type terminal is used to prevent terminal screws from falling off.
*5 Can be used in CNA channels 0 - 7 or CNB channels 32 - 39.
* For details on the range channels available to each terminal panel, see Figure 3.2 "Connecting example of option".

* Check the CONTEC’s Web site for more information on these options.

Customer Support

CONTEC provides the following support services for you to use CONTEC products more efficiently and comfortably.

Web Site

https://www.contec.com/

Latest product information

CONTEC provides up-to-date information on products.

CONTEC also provides product manuals and various technical documents in the PDF.

Free download

You can download updated driver software and differential files as well as sample programs available in several languages.

Note! For product information
Contact your retailer if you have any technical question about a CONTEC product or need its price, delivery time, or estimate information.

Limited Three-Years Warranty

CONTEC products are warranted by CONTEC CO., LTD. to be free from defects in material and workmanship for up to three years from the date of purchase by the original purchaser.

Repair will be free of charge only when this device is returned freight prepaid with a copy of the original invoice and a Return Merchandise Authorization to the distributor or the CONTEC group office, from which it was purchased.

This warranty is not applicable for scratches or normal wear, but only for the electronic circuitry and original boards. The warranty is not applicable if the device has been tampered with or damaged through abuse, mistreatment, neglect, or unreasonable use, or if the original invoice is not included, in which case repairs will be considered beyond the warranty policy.

How to Obtain Service

For replacement or repair, return the device freight prepaid, with a copy of the original invoice. Please obtain a Return Merchandise Authorization number (RMA) from the CONTEC group office where you purchased before returning any product.

* No product will be accepted by CONTEC group without the RMA number.

Liability

The obligation of the warrantor is solely to repair or replace the product. In no event will the warrantor be liable for any incidental or consequential damages due to such defect or consequences that arise from inexperienced usage, misuse, or malfunction of this device.

Safety Precautions

Understand the following definitions and precautions to use the product safely.

Safety Information

This document provides safety information using the following symbols to prevent accidents resulting in injury or death and the destruction of equipment and resources. Understand the meanings of these labels to operate the equipment safely.

⚠ DANGER	DANGER indicates an imminently hazardous situation which, if not avoided, will result in death or serious injury.
⚠ WARNING	WARNING indicates a potentially hazardous situation which, if not avoided, could result in death or serious injury.
⚠ CAUTION	CAUTION indicates a potentially hazardous situation which, if not avoided, may result in minor or moderate injury or in property damage.

Handling Precautions

⚠ DANGER

Do not use the product where it is exposed to flammable or corrosive gas. Doing so may result in an explosion, fire, electric shock, or failure.

⚠ CAUTION

- There are switches and jumpers on the board that need to be set in advance. Be sure to check these before installing the board.
- Only set the switches and jumpers on the board to the specified settings.
  Otherwise, the board may malfunction, overheat, or cause a failure.
- Do not strike or bend the board.
  Otherwise, the board may malfunction, overheat, cause a failure or breakage.
- Do not touch the board's metal plated terminals (edge connector) with your hands.
  Otherwise, the board may malfunction, overheat, or cause a failure.
  If the terminals are touched by someone's hands, clean the terminals with industrial alcohol.
- Do not plug or unplug the cables which are connected to the board while the PC or expansion unit is still turned on.
  Otherwise, the board may malfunction, overheat, or cause a failure.
  Be sure that the personal computer power is turned off.
- Do not install or remove the board to or from the slot while the computer's power is turned on.
  Otherwise, the board may malfunction, overheat, or cause a failure.
  Be sure that the personal computer or the I/O expansion unit power is turned off.
- Make sure that your PC or expansion unit can supply ample power to all the boards installed.
  Insufficiently energized boards could malfunction, overheat, or cause a failure.
- The specifications of this product are subject to change without notice for enhancement and quality improvement.
  Even when using the product continuously, be sure to read the manual and understand the contents.
- Do not modify the product. CONTEC will bear no responsibility for any problems, etc., resulting from modifying this product.
- Regardless of the foregoing statements, CONTEC is not liable for any damages whatsoever (including damages for loss of business profits) arising out of the use or inability to use this CONTEC product or the information contained herein.
- Regarding “CE EMC Directive Notice”.
  Please connect the Interface Connector with a shielded cable to meet the mentioned standard above.

Environment

Use this product in the following environment. If used in an unauthorized environment, the board may overheat, malfunction, or cause a failure.

Operating temperature

0 - 50°C

Operating humidity

10 - 90%RH (No condensation)

Corrosive gases

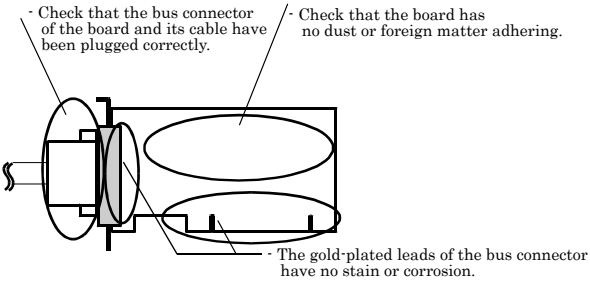
None

Floating dust particles

Not to be excessive

Inspection

Inspect the product periodically as follows to use it safely.



Storage

When storing this product, keep it in its original packing form.

(1) Put the board in the storage bag.
(2) Wrap it in the packing material, then put it in the box.
(3) Store the package at room temperature at a place free from direct sunlight, moisture, shock, vibration, magnetism, and static electricity.

Disposal

When disposing of the product, follow the disposal procedures stipulated under the relevant laws and municipal ordinances.

2. Setup

This chapter explains how to set up the board.

What is Setup?

Setup means a series of steps to take before the product can be used.
Different steps are required for software and hardware.
The setup procedure varies with the OS and applications used.

Using the Board under Windows

Using the Driver Library API-PAC(W32)

This section describes the setup procedure to be performed before you can start developing application programs for the board using the bundled disk "Driver Library API-PAC(W32)".

Taking the following steps sets up the software and hardware. You can use the diagnosis program later to check whether the software and hardware function normally.

Step 1 Installing the Software

Step 2 Setting the Hardware

Step 3 Installing the Hardware

Step 4 Initializing the Software

Step 5 Checking Operations with the Diagnosis Program

If Setup fails to be performed normally, see the "Setup Troubleshooting" section at the end of this chapter.

Using the Board under Windows

Using Software Other than the Driver Library API-PAC(W32)

For setting up software other than API-PAC(W32), refer to the manual for that software. See also the following parts of this manual as required.

This chapter Step 2 Setting the Hardware

This chapter Step 3 Installing the Hardware

Chapter 3 External Connection

Chapter 6 About Hardware

Using the Board under an OS Other than Windows

For using the board under Linux, see the following parts of this manual.

This chapter Step 2 Setting the Hardware

Chapter 3 External Connection

Chapter 5 About Software

Chapter 6 About Hardware

For using the board under an OS other than Windows and Linux, see the following parts of this manual.

This chapter Step 2 Setting the Hardware

Chapter 3 External Connection

Chapter 6 About Hardware

Step 1 Installing the Software

This explains how to install the driver library.

Before installing the hardware on the PC, install the driver library from the API-PAC(W32) disk **provided with the board.**

The following description assumes the operating system as Windows XP. Although some user interfaces are different depending on the OS used, the basic procedure is the same.

Which Driver to Use

CONTEC has two analog I/O drivers: "API-AIO(WDM)" and "API-AIO(98/PC)".

API-AIO(WDM) is a new driver for analog I/O under Windows.

This driver was developed to be easier to use and to provide additional functions above those provided by the previous API-AIO(98/PC) driver.

Please use the API-AIO(WDM) with this board. API-AIO(98/PC) is not supported.

Starting the Install Program

(1) **Load the** bundled disk **[API-PAC(W32)] on your PC.**

(2) **The API-PAC(W32) Installer window appears automatically.**
If the panel does not appear, run (drive letter):\AUTORUN.exe.

(3) **Click on the [Install the drivers] button.**

⚠ CAUTION

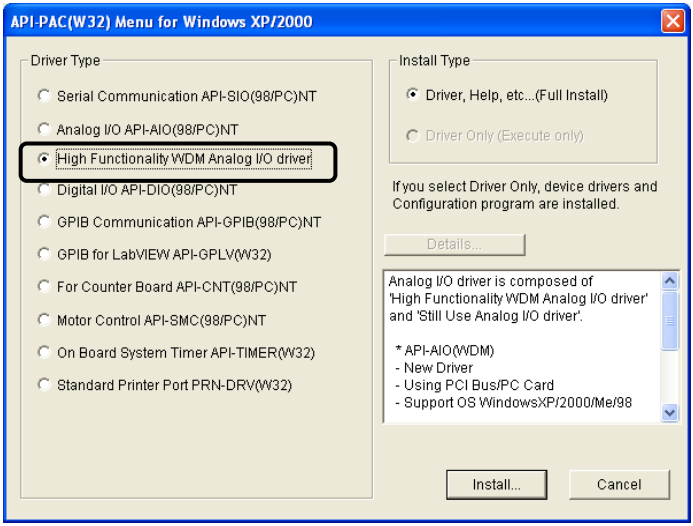
Before installing the software in Windows XP, 2000, or NT, log in as a user with administrator privileges.

API-AIO(WDM)

(1) **The following dialog box appears to select “Driver Type” and “Install Type”.**

(2) **Select the "High Functionality WDM Analog I/O driver".**

(3) **Click on the [Install] button.**



* Clicking the [Details] button displays detailed information about API-AIO(WDM) and API-AIO(98/PC).

Run the installation

(1) **Complete the installation by following the instructions on the screen.**

(2) The Readme file appears when the installation is complete.

You have now finished installing the software.

Step 2 Setting the Hardware

This section describes how to set the board and plug it on your PC.

The board has some switches and jumper to be preset.
Check the on-board switches and jumpers before plugging the board into an expansion slot.

The board can be set up even with the factory defaults untouched. You can change board settings later.

Replacing the Bracket

This board is shipped with a Low Profile size bracket mounted. To plug the board into a standard size slot, replace the bracket with the bundled standard size bracket. The replacing method is as follows :

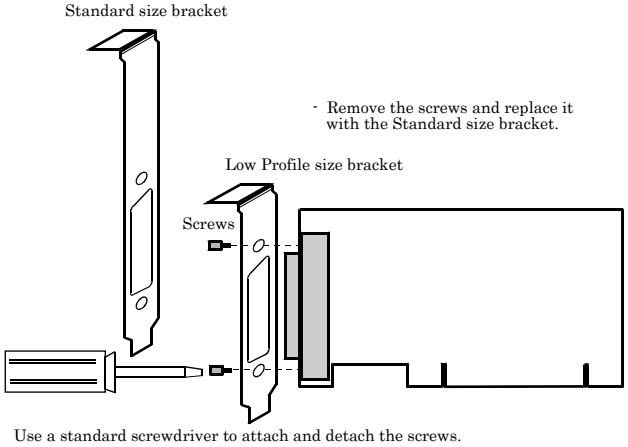


Figure 2.1. Replacing the Bracket

Parts of the Board and Factory Defaults

Figure 2.2 shows the names of each part of the board.

The switch settings shown in the figure are the factory default settings.

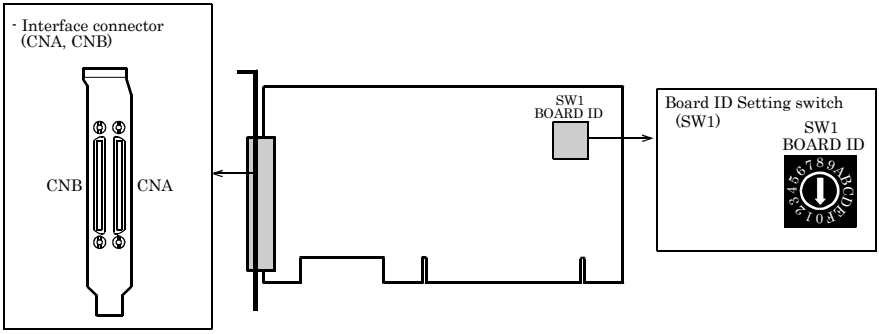


Figure 2.2. Part Names

Setting the Board ID

If you install two or more boards on one personal computer, assign a different ID value to each of the boards to distinguish them.

The board IDs can be set from 0 - Fh to identify up to sixteen boards.

If only one board is used, the original factory setting (Board ID = 0) should be used.

Setting Procedure

To set the board ID, use the rotary switch on the board. Turn the SW1 knob to set the board ID as shown below.

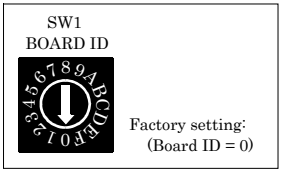


Figure 2.3. Board ID Settings (SW1)

Plugging the Board

(1) Before plugging the board, shut down the system, unplug the power code of your PC.

(2) Remove the cover from the PC so that the board can be mounted.

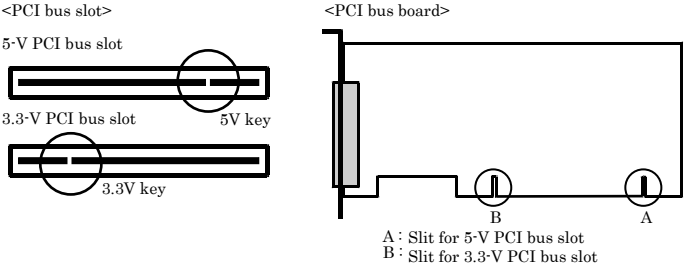
(3) Plug the board into an expansion slot.

(4) Attach the board bracket to the PC with a screw.

(5) Put the cover back into place.

Applicable PCI bus slots

PCI bus slots used in PCs have keys to prevent 5V and 3.3V PCI bus boards from being accidentally plugged into wrong bus slots. This board can be plugged into both of the 5V and 3.3V PCI bus slots.



⚠ CAUTION

- Do not touch the board's metal plated terminals (edge connector) with your hands.
  Otherwise, the board may malfunction, overheat, or cause a failure.
  If the terminals are touched by someone's hands, clean the terminals with industrial alcohol.
- Do not install or remove the board to or from the slot while the computer's power is turned on.
  Otherwise, the board may malfunction, overheat, or cause a failure.
  Doing so could cause trouble. Be sure that the personal computer or the I/O expansion unit power is turned off.
- Make sure that your PC or expansion unit can supply ample power to all the boards installed.
  Insufficiently energized boards could malfunction, overheat, or cause a failure.
- Power supply from the PCI bus slot at +5V is required.

Step 3 Installing the Hardware

Windows needs to detect the I/O address and interrupt used by the board. This is called hardware installation.

When using more than one board, install the boards one at a time and do not install the next board until setup is complete for the previous board.

Turning on the PC

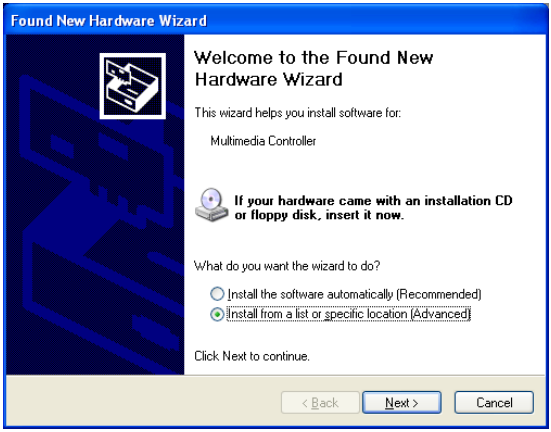
Turn on the power to your PC.

⚠ CAUTION

- The board cannot be properly installed unless the resources (I/O addresses and interrupt level) for the board can be allocated. Before attempting to install the board, first determine what PC resources are free to use.
- The resources used by each board do not depend on the location of the PCI bus slot or the board itself. If you remove two or more boards that have already been installed and then remount one of them on the computer, it is unknown that which one of the sets of resources previously assigned to the two boards is assigned to the remounted board. In this case, you must check the resource settings.

When Using API-AIO(WDM)

(1) The “Found New Hardware Wizard” will be started.
Select “Install from a list or specific location[Advanced]”, then click on the [Next] button.

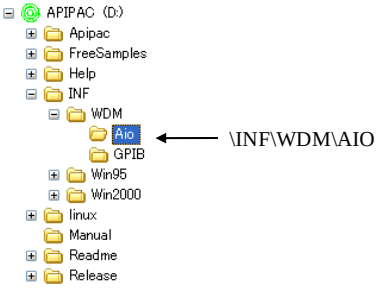


(2) Specify that folder on the bundled disk which contains the setup information (INF) file to register the board.

Source folder

The setup information (INF) file is contained in the following folder on the bundled disk.

\INF\WDM\AIO



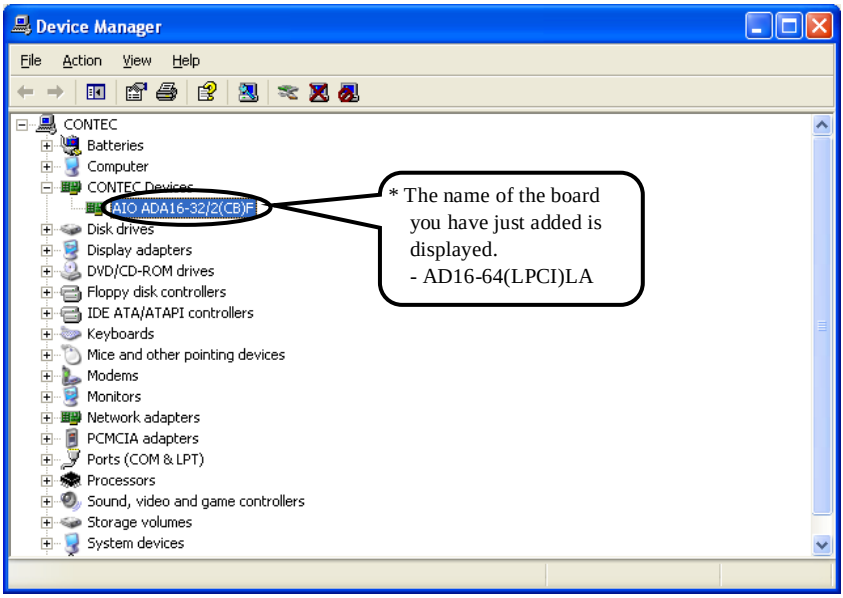
You have now finished installing the hardware.

Step 4 Initializing the Software

The driver library requires initial settings to determine the execution environment. This is called driver library initialization.

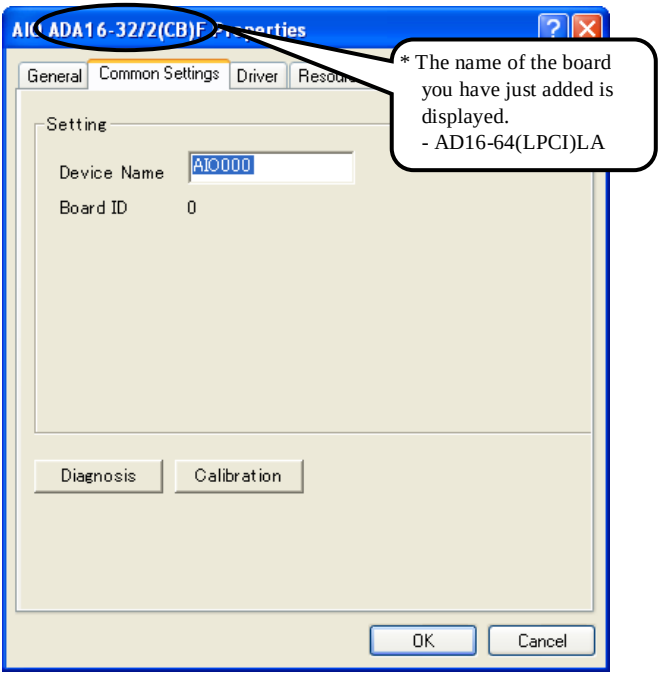
Setting the device name

(1) Run Device Manager. From [My Computer] - [Control Panel], select [System] and then select the [Device Manager] tab.
(You can also open Device Manager by right clicking on My Computer and selecting Properties.)



(2) The installed hardware appears under the CONTEC Devices node. Open the CONTEC Devices node and select the device you want to setup (the device name should appear highlighted). Click [Properties].

(3) The property page for the device opens.
Enter the device name in the common settings tab page and then click [OK].
The device name you set here is used later when programming.



* The initial device name that appears is a default value. You can use this default name if you wish.

* Make sure that you do not use the same name for more than one device.

You have now finished installing the initial setting of Software.

Step 5 Checking Operations with the Diagnosis Program

Use the diagnosis program to check that the board and driver software work normally, thereby you can confirm that they have been set up correctly.

What is the Diagnosis Program?

The diagnosis program diagnoses the states of the board and driver software.
It can also be used as a simple checker when an external device is actually connected.
Using the "Diagnosis Report" feature reports the driver settings, the presence or absence of the board, I/O status, and interrupt status.

Check Method

To check the analog input data, connect to an external signal source.
The figure below shows an example of checking by connecting to an external signal.
The example below is for analog input channel 0 on the AD16-64(LPCI)LA.

Wiring Diagram

< Analog input >

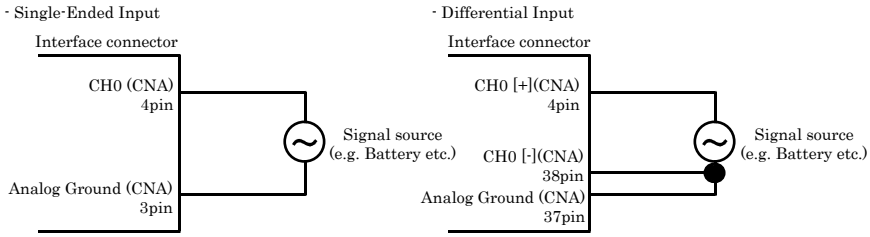


Figure 2.4. Wiring Diagram

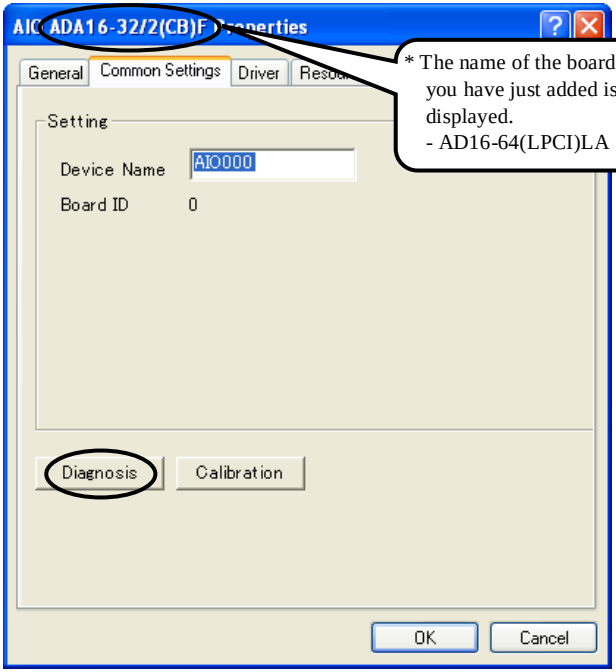
⚠ CAUTION

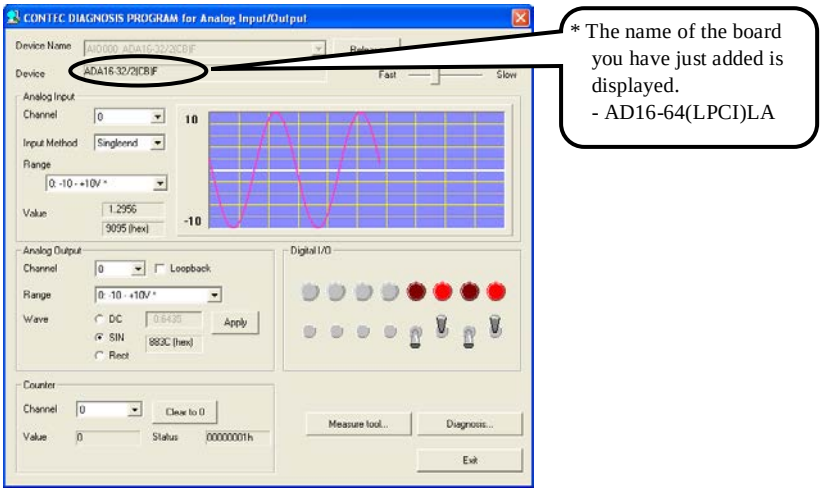
- Input data remains indeterminate when no input pin is connected. The input pin for the channel not connected to the signal source must be connected to the analog ground.
  For details, see "Chapter 3 External Connection".

Using the Diagnosis Program

Starting the Diagnosis Program

Click the [Diagnosis] button on the device property page to start the diagnosis program.





Analog input

Select the input channel, input type, and input range from the lists.

Input data is plotted on a graph.

Digital I/O

The upper row of circular lamps indicates the digital input states. Red indicates the bit is ON and brown indicates OFF.

Clicking the lower row of switches turns the digital output bits ON or OFF.

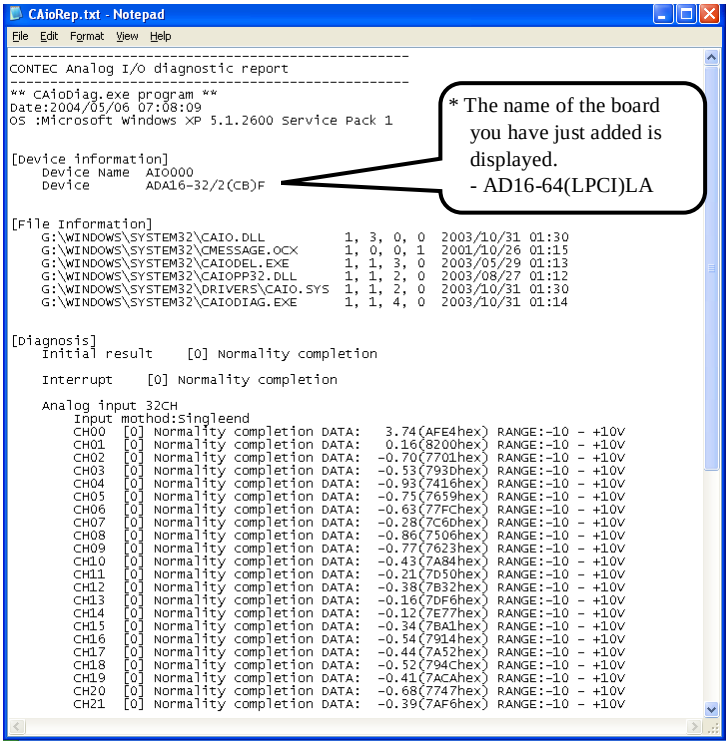
Counter input

Selecting a counter channel displays the count value and state of that counter channel.
Clicking the zero clear button resets the count to zero.

Diagnosis Report

(1) The diagnosis report saves detailed data, including the device settings and settings for each channel, to a text file and displays the file for you to view.

Clicking [Diagnosis Report] prompts you to specify where to save the report text file.




```
------------------------------------------------------
CONTEC Analog I/O diagnostic report
------------------------------------------------------
** CAioDiag.exe program **
Date:2004/05/06 07:08:09
OS :Microsoft Windows XP 5.1.2600 Service Pack 1

[Device information]
    Device Name  AIO000
    Device       ADA16-32/2(CB)F

[File Information]
    G:\WINDOWS\SYSTEM32\CAIO.DLL          1, 3, 0, 0  2003/10/31 01:30
    G:\WINDOWS\SYSTEM32\CMESSAGE.OCX      1, 0, 0, 1  2001/10/26 01:15
    G:\WINDOWS\SYSTEM32\CAIODEL.EXE       1, 1, 3, 0  2003/05/29 01:13
    G:\WINDOWS\SYSTEM32\CAIOPP32.DLL      1, 1, 2, 0  2003/08/27 01:12
    G:\WINDOWS\SYSTEM32\DRIVERS\CAIO.SYS  1, 1, 2, 0  2003/10/31 01:30
    G:\WINDOWS\SYSTEM32\CAIODIAG.EXE      1, 1, 4, 0  2003/10/31 01:14

[Diagnosis]
    Initial result    [0] Normality completion

    Interrupt     [0] Normality completion

    Analog input 32CH
        Input mothod:singleend
        CH00  [0] Normality completion DATA:   3.74(AFE4hex) RANGE:-10 - +10V
        CH01  [0] Normality completion DATA:   0.16(8200hex) RANGE:-10 - +10V
        CH02  [0] Normality completion DATA:  -0.70(7701hex) RANGE:-10 - +10V
        CH03  [0] Normality completion DATA:  -0.53(793Dhex) RANGE:-10 - +10V
        CH04  [0] Normality completion DATA:  -0.93(7416hex) RANGE:-10 - +10V
        CH05  [0] Normality completion DATA:  -0.75(7659hex) RANGE:-10 - +10V
        CH06  [0] Normality completion DATA:  -0.63(77FChex) RANGE:-10 - +10V
        CH07  [0] Normality completion DATA:  -0.28(7C6Dhex) RANGE:-10 - +10V
        CH08  [0] Normality completion DATA:  -0.86(7506hex) RANGE:-10 - +10V
        CH09  [0] Normality completion DATA:  -0.77(7623hex) RANGE:-10 - +10V
        CH10  [0] Normality completion DATA:  -0.43(7A84hex) RANGE:-10 - +10V
        CH11  [0] Normality completion DATA:  -0.21(7D50hex) RANGE:-10 - +10V
        CH12  [0] Normality completion DATA:  -0.38(7B32hex) RANGE:-10 - +10V
        CH13  [0] Normality completion DATA:  -0.16(7DF6hex) RANGE:-10 - +10V
        CH14  [0] Normality completion DATA:  -0.12(7E77hex) RANGE:-10 - +10V
        CH15  [0] Normality completion DATA:  -0.34(7BA1hex) RANGE:-10 - +10V
        CH16  [0] Normality completion DATA:  -0.54(7914hex) RANGE:-10 - +10V
        CH17  [0] Normality completion DATA:  -0.44(7A52hex) RANGE:-10 - +10V
        CH18  [0] Normality completion DATA:  -0.52(794Chex) RANGE:-10 - +10V
        CH19  [0] Normality completion DATA:  -0.41(7ACAhex) RANGE:-10 - +10V
        CH20  [0] Normality completion DATA:  -0.68(7747hex) RANGE:-10 - +10V
        CH21  [0] Normality completion DATA:  -0.39(7AF6hex) RANGE:-10 - +10V
```

(2) The diagnosis report contains the following data.

- Version of OS
- Device Information
- File Information
- Initialization, interrupts, current input or output state for each channel

Setup Troubleshooting

Symptoms and Actions

Data input does not operate correctly

- Run the diagnosis program to check that the device is registered and whether any initialization errors have occurred.
- Is there a problem with the device settings, wiring, or similar? Check the input range setting. Also, the input data will be undefined if the wiring terminals are not connected. Ensure that the channels you are using are correctly connected. Connect unused channels to analog ground.
- For voltage input, check by connecting a battery or similar if you do not have any other suitable signal source. Also check that a prescribed voltage can input the channel by the short-circuit as an analog ground.

The diagnostic program works correctly but the application program does not.

The diagnostic program uses the API-TOOL functions. If the diagnostic program works correctly, other applications should work correctly also. If you have a problem, recheck your program taking note of the following points.

- Check the return values of the API functions.
- Refer to the source code for the sample programs.

The OS does not boot correctly or does not detect the device correctly.

Refer to the "Troubleshooting" section of API-AIO(WDM) HELP.

If your problem cannot be resolved

Contact your retailer.

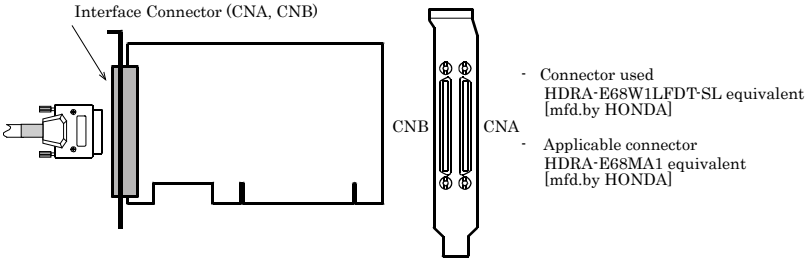
3. External Connection

This chapter describes the interface connectors on the board and the external I/O circuits.
Check the information available here when connecting an external device.

How to connect the connectors

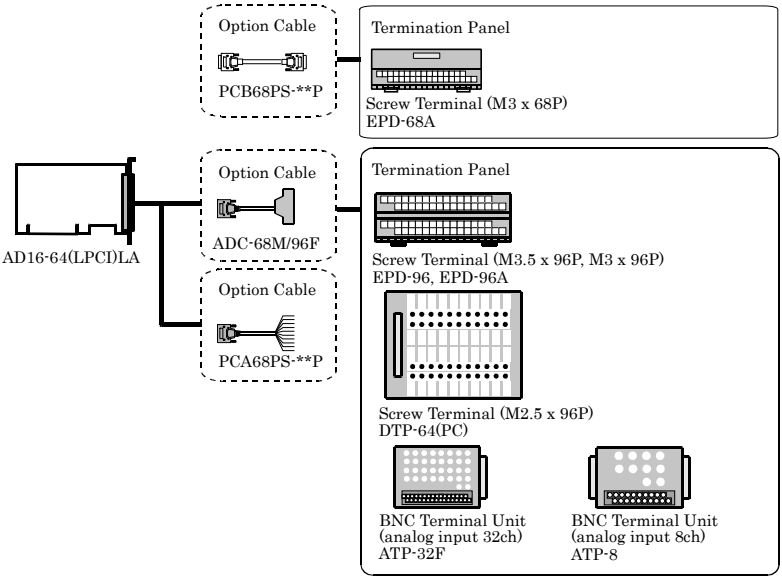
Connector shape

The optional connector cable (PCB68PS-**P, ADC-68M/96F or PCA68PS-**P) is used to connect the board to external devices. The cable is used together with a terminal block to connect external devices. Two sets of cables are required depending on the number of channels used.



* Please refer to chapter 1 for more information on the supported cable and accessories.

Figure 3.1. Interface Connector Shape



Each terminal block accepts the following ranges of channels.

	Connector at board side connection destination	Analog input		Analog input control signal *1	Digital input Digital output	Counter I/O *2
		Single-ended input	Differential input			
EPD-96A EPD-96 EPD-68A DTP-64	Only CNA is used.	channel 0 - 31	channel 0 - 15	O	O	O
	Only CNB is used.	channel 32 - 63	channel 16 - 31	--	--	--
	CNA/B is used *3	channel 0 - 63	channel 0 - 31	O *4	O *4	O *4
ATP-32F	Only CNA is used.	channel 0 - 31	--	O	O	O
	Only CNB is used.	channel 32 - 63	--	--	--	--
	CNA/B is used *3	channel 0 - 63	--	O *4	O *4	O *4
ATP-8	Only CNA is used.	channel 0 - 7	--	O	O	O
	Only CNB is used.	channel 32 - 39 *5	--	--	--	--
	CNA/B is used *3	channel 0 - 7, 32 - 39 *5	--	O *4	O *4	O *4

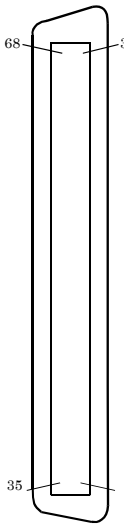
*1 : AI External Start Trigger Input, AI External StopTrigger Input, AI External Clock Trigger Input
*2 : Counter Gate Control Input, Counter Up Clock Input, Counter Output
*3 : Two sets of terminal blocks and optional cables are required each.
*4 : Make wiring on the CAN side.
*5 : Two or more only of channel 32 - 39 sampling cannot be done.

Figure 3.2. Connecting example of option

Connector Pin Assignment

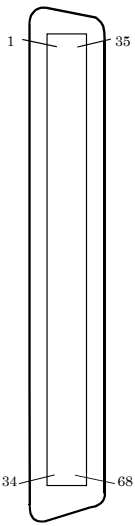
Single-Ended Input (CNA, CNB)

N.C	68	34	N.C
N.C	67	33	N.C
N.C	66	32	N.C
N.C	65	31	N.C
N.C	64	30	N.C
N.C	63	29	N.C
N.C	62	28	N.C
Digital Ground	61	27	N.C
N.C	60	26	N.C
N.C	59	25	N.C
Digital Ground	58	24	N.C
N.C	57	23	N.C
Analog Input 63	56	22	Analog Input 47
Analog Input 62	55	21	Analog Input 46
Analog Input 61	54	20	Analog Input 45
Analog Input 60	53	19	Analog Input 44
Analog Ground (for AI)	52	18	Analog Ground (for AI)
Analog Input 59	51	17	Analog Input 43
Analog Input 58	50	16	Analog Input 42
Analog Input 57	49	15	Analog Input 41
Analog Input 56	48	14	Analog Input 40
Analog Ground (for AI)	47	13	Analog Ground (for AI)
Analog Input 55	46	12	Analog Input 39
Analog Input 54	45	11	Analog Input 38
Analog Input 53	44	10	Analog Input 37
Analog Input 52	43	9	Analog Input 36
Analog Ground (for AI)	42	8	Analog Ground (for AI)
Analog Input 51	41	7	Analog Input 35
Analog Input 50	40	6	Analog Input 34
Analog Input 49	39	5	Analog Input 33
Analog Input 48	38	4	Analog Input 32
Analog Ground (for AI)	37	3	Analog Ground (for AI)
Analog Ground (for AI)	36	2	N.C
Analog Ground (for AI)	35	1	N.C



CNB

N.C.	1	35	Analog Ground (for AI)
N.C.	2	36	Analog Ground (for AI)
Analog Ground (for AI)	3	37	Analog Ground (for AI)
Analog Input 00	4	38	Analog Input 16
Analog Input 01	5	39	Analog Input 17
Analog Input 02	6	40	Analog Input 18
Analog Input 03	7	41	Analog Input 19
Analog Ground (for AI)	8	42	Analog Ground (for AI)
Analog Input 04	9	43	Analog Input 20
Analog Input 05	10	44	Analog Input 21
Analog Input 06	11	45	Analog Input 22
Analog Input 07	12	46	Analog Input 23
Analog Ground (for AI)	13	47	Analog Ground (for AI)
Analog Input 08	14	48	Analog Input 24
Analog Input 09	15	49	Analog Input 25
Analog Input 10	16	50	Analog Input 26
Analog Input 11	17	51	Analog Input 27
Analog Ground (for AI)	18	52	Analog Ground (for AI)
Analog Input 12	19	53	Analog Input 28
Analog Input 13	20	54	Analog Input 29
Analog Input 14	21	55	Analog Input 30
Analog Input 15	22	56	Analog Input 31
Input Control External Sampling Start Trigger Input	23	57	Input Control External Sampling Stop Trigger Input
Input Control External Sampling Clock Input	24	58	Digital Ground
N.C.	25	59	N.C.
N.C.	26	60	N.C.
N.C.	27	61	Digital Ground
N.C.	28	62	N.C.
Digital Input 00	29	63	Digital Input 01
Digital Input 02	30	64	Digital Input 03
Digital Output 00	31	65	Digital Output 01
Digital Output 02	32	66	Digital Output 03
Counter Gate Control Input	33	67	Counter Count-up Pulse Output
Counter Clock Input	34	68	Reserved (Counter Input)



CNA

Analog Input00 - Analog Input63	Analog input signal. The numbers correspond to channel numbers.
Analog Ground	Common analog ground for analog input signals.
AI External Start Trigger Input	External trigger input for starting analog input sampling.
AI External Stop Trigger Input	External trigger input for stopping analog input sampling.
AI External Sampling Clock Input	External sampling clock input for analog input.
Digital Input00 - Digital Input03	Digital input signal.
Digital Output00 - Digital Output03	Digital output signal.
Counter Gate Control Input	Gate control input signal for counter.
Counter Up Clock Input	Count-up clock input signal for counter.
Counter Output	Count output signal.
Digital Ground	Common digital ground for digital I/O signals, external trigger inputs,
Reserved	Reserved pin
N.C.	No connection to this pin.

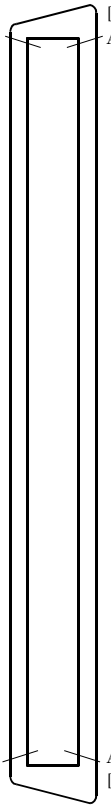
Figure 3.3. Pin Assignment of interface connector (Single-Ended Input)

⚠ CAUTION

- Do not connect any of the outputs and power outputs to the analog or digital ground. Neither connect outputs to each other. Doing either can result in a fault.
- If analog and digital ground are shorted together, noise on the digital signals may affect the analog signals. Accordingly, analog and digital ground should be separated.
- Leave "Reserved" pins unconnected. Connecting these pins may cause a fault in the board.

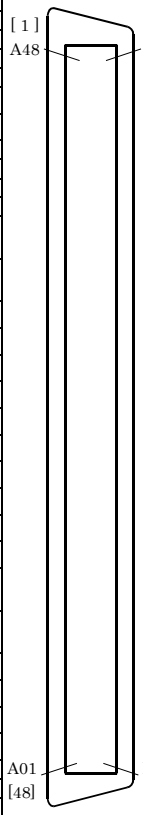
Single-Ended Input (ADC-68M/96F)

N.C.	B01	A01	N.C	
N.C.	B02	A02	N.C	
N.C.	B03	A03	N.C	
N.C.	B04	A04	N.C	
N.C	B05	A05	N.C	
N.C	B06	A06	N.C	
N.C	B07	A07	N.C	
Digital Ground	B08	A08	Digital Ground	
N.C	B09	A09	N.C	
N.C	B10	A10	N.C	
N.C.	B11	A11	N.C.	
N.C.	B12	A12	N.C.	
N.C.	B13	A13	N.C.	
N.C.	B14	A14	N.C.	
N.C	B15	A15	N.C	
N.C	B16	A16	N.C	
N.C	B17	A17	N.C	
N.C	B18	A18	N.C	
N.C.	B19	A19	N.C.	
N.C.	B20	A20	N.C.	
Analog Ground (for AI)	B21	A21	Analog Ground (for AI)	
Analog Ground (for AI)	B22	A22	Analog Ground (for AI)	
Analog Input 63	B23	A23	Analog Input 55	
Analog Input 47	B24	A24	Analog Input 39	
Analog Input 62	B25	A25	Analog Input 54	
Analog Input 46	B26	A26	Analog Input 38	
N.C.	B27	A27	N.C.	
N.C.	B28	A28	N.C.	
Analog Input 61	B29	A29	Analog Input 53	
Analog Input 45	B30	A30	Analog Input 37	
Analog Input 60	B31	A31	Analog Input 52	
Analog Input 44	B32	A32	Analog Input 36	
Analog Ground (for AI)	B33	A33	Analog Ground (for AI)	
Analog Ground (for AI)	B34	A34	Analog Ground (for AI)	
Analog Input 59	B35	A35	Analog Input 51	
Analog Input 43	B36	A36	Analog Input 35	
Analog Input 58	B37	A37	Analog Input 50	
Analog Input 42	B38	A38	Analog Input 34	
N.C.	B39	A39	N.C.	
N.C.	B40	A40	N.C.	
Analog Input 57	B41	A41	Analog Input 49	
Analog Input 41	B42	A42	Analog Input 33	
Analog Input 56	B43	A43	Analog Input 48	
Analog Input 40	B44	A44	Analog Input 32	
N.C.	B45	A45	Analog Ground	
N.C.	B46	A46	N.C	
N.C.	B47	A47	Analog Ground (for AI)	
N.C.	B48	A48	N.C	



CNB

N.C.	A48	B48	N.C.	
Analog Ground(for AI)	A47	B47	N.C.	
N.C.	A46	B46	N.C.	
Analog Ground(for AI)	A45	B45	N.C.	
Analog Input 00	A44	B44	Analog Input 08	
Analog Input 16	A43	B43	Analog Input 24	
Analog Input 01	A42	B42	Analog Input 09	
Analog Input 17	A41	B41	Analog Input 25	
N.C.	A40	B40	N.C	
N.C.	A39	B39	N.C	
Analog Input 02	A38	B38	Analog Input 10	
Analog Input 18	A37	B37	Analog Input 26	
Analog Input 03	A36	B36	Analog Input 11	
Analog Input 19	A35	B35	Analog Input 27	
Analog Ground (for AI)	A34	B34	Analog Ground (for AI)	
Analog Ground (for AI)	A33	B33	Analog Ground (for AI)	
Analog Input 04	A32	B32	Analog Input 12	
Analog Input 20	A31	B31	Analog Input 28	
Analog Input 05	A30	B30	Analog Input 13	
Analog Input 21	A29	B29	Analog Input 29	
N.C.	A28	B28	N.C.	
N.C.	A27	B27	N.C.	
Analog Input 06	A26	B26	Analog Input 14	
Analog Input 22	A25	B25	Analog Input 30	
Analog Input 07	A24	B24	Analog Input 15	
Analog Input 23	A23	B23	Analog Input 31	
Analog Ground (for AI)	A22	B22	Analog Ground (for AI)	
Analog Ground (for AI)	A21	B21	Analog Ground (for AI)	
N.C.	A20	B20	N.C.	
N.C.	A19	B19	N.C.	
Digital Input 00	A18	B18	Digital Output 00	
Digital Input 01	A17	B17	Digital Output 01	
Digital Input 02	A16	B16	Digital Output 02	
Digital Input 03	A15	B15	Digital Output 03	
N.C.	A14	B14	N.C.	
N.C.	A13	B13	N.C.	
N.C.	A12	B12	N.C.	
N.C.	A11	B11	N.C.	
N.C.	A10	B10	N.C.	
N.C.	A09	B09	N.C.	
Digital Ground	A08	B08	Digital Ground	
Input Control External Sampling Clock Input	A07	B07	N.C.	
Input Control External Sampling Stop Trigger Input	A06	B06	N.C.	
Input Control External Sampling Start Trigger Input	A05	B05	N.C.	
Counter Clock	A04	B04	N.C.	
Reserved (Counter Input)	A03	B03	N.C.	
Counter Gate Control Input	A02	B02	N.C.	
Counter Count-up Pulse Output	A01	B01	N.C.	



CNA

- [ ] shows the pin No. specified by HONDA TSUSHIN KOGYO CO., LTD.

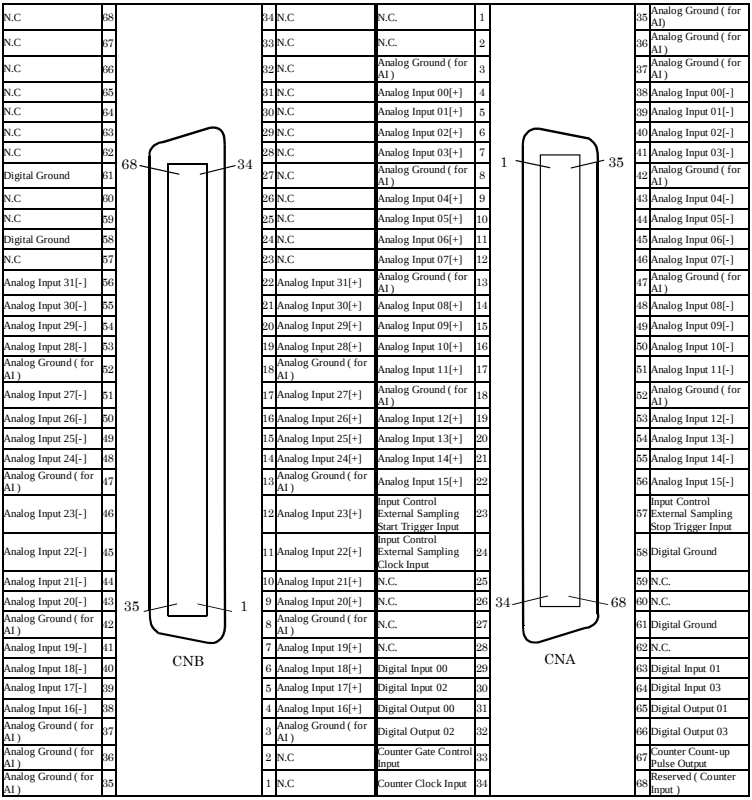
Analog Input00 - Analog Input63	Analog input signal. The numbers correspond to channel numbers.
Analog Ground	Common analog ground for analog input signals.
AI External Start Trigger Input	External trigger input for starting analog input sampling.
AI External Stop Trigger Input	External trigger input for stopping analog input sampling.
AI External Sampling Clock Input	External sampling clock input for analog input.
Digital Input00 - Digital Input03	Digital input signal.
Digital Output00 - Digital Output03	Digital output signal.
Counter Gate Control Input	Gate control input signal for counter.
Counter Up Clock Input	Count-up clock input signal for counter.
Counter Output	Count output signal.
Digital Ground	Common digital ground for digital I/O signals, external trigger inputs,
Reserved	Reserved pin
N.C.	No connection to this pin.

Figure 3.4. Pin Assignment of interface connector (Single-Ended Input)

⚠ CAUTION

- Do not connect any of the outputs and power outputs to the analog or digital ground. Neither connect outputs to each other. Doing either can result in a fault.
- If analog and digital ground are shorted together, noise on the digital signals may affect the analog signals. Accordingly, analog and digital ground should be separated.
- Leave "Reserved" pins unconnected. Connecting these pins may cause a fault in the board.

Differential Input (CNA, CNB)



Analog Input00 - Analog Input31	Analog input signal. The numbers correspond to channel numbers.
Analog Ground	Common analog ground for analog input signals.
AI External Start Trigger Input	External trigger input for starting analog input sampling.
AI External Stop Trigger Input	External trigger input for stopping analog input sampling.
AI External Sampling Clock Input	External sampling clock input for analog input.
Digital Input00 - Digital Input03	Digital input signal.
Digital Output00 - Digital Output03	Digital output signal.
Counter Gate Control Input	Gate control input signal for counter.
Counter Up Clock Input	Count-up clock input signal for counter.
Counter Output	Count output signal.
Digital Ground	Common digital ground for digital I/O signals, external trigger inputs,
Reserved	Reserved pin
N.C.	No connection to this pin.

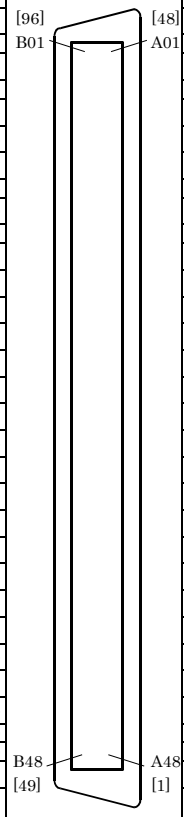
Figure 3.5. Pin Assignment of interface connector (Differential Input)

⚠ CAUTION

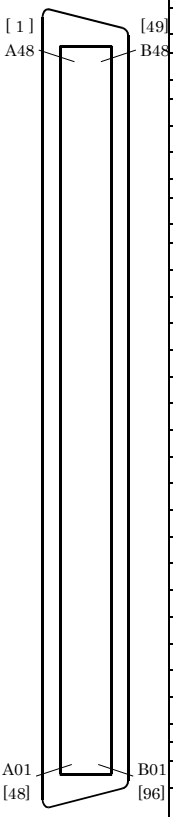
- Do not connect any of the outputs and power outputs to the analog or digital ground. Neither connect outputs to each other. Doing either can result in a fault.
- If analog and digital ground are shorted together, noise on the digital signals may affect the analog signals. Accordingly, analog and digital ground should be separated.
- Leave "Reserved" pins unconnected. Connecting these pins may cause a fault in the board.

Differential Input (ADC-68M/96F)

N.C	B01	A01	N.C	
N.C	B02	A02	N.C	
N.C	B03	A03	N.C	
N.C	B04	A04	N.C	
N.C	B05	A05	N.C	
N.C	B06	A06	N.C	
N.C	B07	A07	N.C	
Digital Ground	B08	A08	Digital Ground	
N.C	B09	A09	N.C	
N.C	B10	A10	N.C	
N.C	B11	A11	N.C	
N.C	B12	A12	N.C	
N.C	B13	A13	N.C	
N.C	B14	A14	N.C	
N.C	B15	A15	N.C	
N.C	B16	A16	N.C	
N.C	B17	A17	N.C	
N.C	B18	A18	N.C	
N.C	B19	A19	N.C	
N.C	B20	A20	N.C	
Analog Ground (for AI)	B21	A21	Analog Ground (for AI)	
Analog Ground (for AI)	B22	A22	Analog Ground (for AI)	
Analog Input 31[-]	B23	A23	Analog Input 23[-]	
Analog Input 31[+]	B24	A24	Analog Input 23[+]	
Analog Input 30[-]	B25	A25	Analog Input 22[-]	
Analog Input 30[+]	B26	A26	Analog Input 22[+]	
N.C	B27	A27	N.C	
N.C	B28	A28	N.C	
Analog Input 29[-]	B29	A29	Analog Input 21[-]	
Analog Input 29[+]	B30	A30	Analog Input 21[+]	
Analog Input 28[-]	B31	A31	Analog Input 20[-]	
Analog Input 28[+]	B32	A32	Analog Input 20[+]	
Analog Ground (for AI)	B33	A33	Analog Ground (for AI)	
Analog Ground (for AI)	B34	A34	Analog Ground (for AI)	
Analog Input 27[-]	B35	A35	Analog Input 19[-]	
Analog Input 27[+]	B36	A36	Analog Input 19[+]	
Analog Input 26[-]	B37	A37	Analog Input 18[-]	
Analog Input 26[+]	B38	A38	Analog Input 18[+]	
N.C	B39	A39	N.C	
N.C	B40	A40	N.C	
Analog Input 25[-]	B41	A41	Analog Input 17[-]	
Analog Input 25[+]	B42	A42	Analog Input 17[+]	
Analog Input 24[-]	B43	A43	Analog Input 16[-]	
Analog Input 24[+]	B44	A44	Analog Input 16[+]	
N.C	B45	A45	Analog Ground (for AI)	
N.C	B46	A46	N.C	
N.C	B47	A47	Analog Ground (for AI)	
N.C	B48	A48	N.C	



N.C	A48	B48	N.C	
Analog Ground (for AI)	A47	B47	N.C	
N.C.	A46	B46	N.C	
Analog Ground (for AI)	A45	B45	N.C	
Analog Input 00[+]	A44	B44	Analog Input 08[+]	
Analog Input 00[-]	A43	B43	Analog Input 08[-]	
Analog Input 01[+]	A42	B42	Analog Input 09[+]	
Analog Input 01[-]	A41	B41	Analog Input 09[-]	
N.C	A40	B40	N.C	
N.C	A39	B39	N.C	
Analog Input 02[+]	A38	B38	Analog Input 10[+]	
Analog Input 02[-]	A37	B37	Analog Input 10[-]	
Analog Input 03[+]	A36	B36	Analog Input 11[+]	
Analog Input 03[-]	A35	B35	Analog Input 11[-]	
Analog Ground (for AI)	A34	B34	Analog Ground (for AI)	
Analog Ground (for AI)	A33	B33	Analog Ground (for AI)	
Analog Input 04[+]	A32	B32	Analog Input 12[+]	
Analog Input 04[-]	A31	B31	Analog Input 12[-]	
Analog Input 05[+]	A30	B30	Analog Input 13[+]	
Analog Input 05[-]	A29	B29	Analog Input 13[-]	
N.C	A28	B28	N.C	
N.C	A27	B27	N.C	
Analog Input 06[+]	A26	B26	Analog Input 14[+]	
Analog Input 06[-]	A25	B25	Analog Input 14[-]	
Analog Input 07[+]	A24	B24	Analog Input 15[+]	
Analog Input 07[-]	A23	B23	Analog Input 15[-]	
Analog Ground (for AI)	A22	B22	Analog Ground (for AI)	
Analog Ground (for AI)	A21	B21	Analog Ground (for AI)	
N.C	A20	B20	N.C	
N.C	A19	B19	N.C	
Digital Input 00	A18	B18	Digital Output 00	
Digital Input 01	A17	B17	Digital Output 01	
Digital Input 02	A16	B16	Digital Output 02	
Digital Input 03	A15	B15	Digital Output 03	
N.C	A14	B14	N.C	
N.C	A13	B13	N.C	
N.C	A12	B12	N.C	
N.C	A11	B11	N.C	
N.C	A10	B10	N.C	
N.C	A09	B09	N.C	
Digital Ground	A08	B08	Digital Ground	
Input Control External Sampling Clock Input	A07	B07	N.C.	
Input Control External Sampling Stop Trigger Input	A06	B06	N.C.	
Input Control External Sampling Start Trigger Input	A05	B05	N.C.	
Counter Clock Input	A04	B04	N.C	
Reserved (Counter Input)	A03	B03	N.C	
Counter Gate Control Input	A02	B02	N.C	
Counter Count-up Pulse Output	A01	B01	N.C	



- [ ] shows the pin No. specified by HONDA TSUSHIN KOGYO CO., LTD.

Analog Input00 - Analog Input31	Analog input signal. The numbers correspond to channel numbers.
Analog Ground	Common analog ground for analog input signals.
AI External Start Trigger Input	External trigger input for starting analog input sampling.
AI External Stop Trigger Input	External trigger input for stopping analog input sampling.
AI External Sampling Clock Input	External sampling clock input for analog input.
Digital Input00 - Digital Input03	Digital input signal.
Digital Output00 - Digital Output03	Digital output signal.
Counter Gate Control Input	Gate control input signal for counter.
Counter Up Clock Input	Count-up clock input signal for counter.
Counter Output	Count output signal.
Digital Ground	Common digital ground for digital I/O signals, external trigger inputs,
Reserved	Reserved pin
N.C.	No connection to this pin.

Figure 3.6. Pin Assignment of interface connector (Differential Input)

⚠ CAUTION

- Do not connect any of the outputs and power outputs to the analog or digital ground. Neither connect outputs to each other. Doing either can result in a fault.
- If analog and digital ground are shorted together, noise on the digital signals may affect the analog signals. Accordingly, analog and digital ground should be separated.
- Leave "Reserved" pins unconnected. Connecting these pins may cause a fault in the board.

Analog Input Signal Connection

The procedure for connecting analog signals depends on whether the analog input signals are single-ended or differential. The sections below describe how to connect the signals using flat cable and shielded cable.

Single-ended Input

The following figure shows an example of flat cable connection.
Connect separate signal and ground wires for each analog input channel on CNA/B.

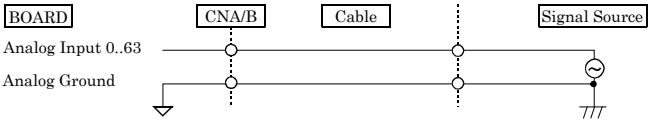


Figure 3.5. Single-ended Input Connection (Flat Cable)

The following figure shows an example of shield cable connection. Use shielded cable if the distance between the signal source and board is long or if you want to provide better protection from noise. For each analog input channel on CNA/B, connect the core wire to the signal line and connect the shielding to ground.

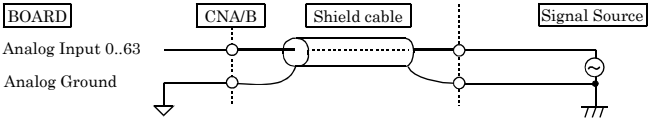


Figure 3.6. Single-ended Input Connection (Shield Cable)

⚠ CAUTION

- If the signal source contains over 1MHz signals, the signal may effect the cross-talk noise between channels.
- If the board and the signal source receive noise or the distance between the board and the signal source is too long, data may not be input properly.
- An input analog signal should not exceed the maximum input voltage (relate to the board analog ground). If it exceeds the maximum voltage, the board may be damaged.
- Connect all the unused analog input channels to analog ground.
- The signal connected to an input pin may fluctuate after switching of the multiplexer. If this occurs, shorten the cable between the signal source and the analog input pin or insert a high-speed amplifier as a buffer between the two to reduce the fluctuation.
- An input pin may fail to obtain input data normally when the signal source connected to the pin has high impedance. If this is the case, change the signal source to one with lower output impedance or insert a high-speed amplifier buffer between the signal source and the analog input pin to reduce the effect.

Differential Input

The following figure shows an example of flat cable connection.
For each analog input channel on CNA/B, connect the "+" input to the signal and connect the "-" input to the signal source ground. Also connect the analog ground on the board to the signal source ground.

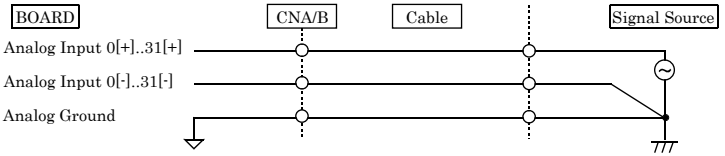


Figure 3.7. Differential Input Connection (Flat Cable)

The following figure shows an example of shielded cable connection. Use shielded cable if the distance between the signal source and board is long or if you want to provide better protection from noise. For each analog input channel on CNA/B, connect the "+" input to the signal and connect the "-" input to the signal source ground. Also connect the analog ground on the board and the signal source ground to the shielding.

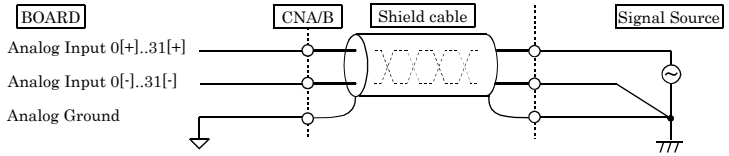


Figure 3.8. Differential Input Connection (Shield Cable)

⚠ CAUTION

- If the signal source contains over 1MHz signals, the signal may effect the cross-talk noise between channels.
- When the analog ground is not connected, the conversion data is not determined.
- If the board and the signal source receive noise or the distance between the board and the signal source is too long, data may not be input properly.
- An input analog signal should not exceed the maximum input voltage (relate to the board analog ground). If it exceeds the maximum voltage, the board may be damaged.
- Connect all the unused analog input channels to analog ground.
- The signal connected to an input pin may fluctuate after switching of the multiplexer. If this occurs, shorten the cable between the signal source and the analog input pin or insert a high-speed amplifier as a buffer between the two to reduce the fluctuation.
- An input pin may fail to obtain input data normally when the signal source connected to the pin has high impedance. If this is the case, change the signal source to one with lower output impedance or insert a high-speed amplifier buffer between the signal source and the analog input pin to reduce the effect.

Digital I/O signals, Counter signals and Control signals Connection

The following sections show examples of how to connect digital I/O signals, counter I/O signals, and other control I/O signals (external trigger input signals, sampling clock input signals, etc.).

All the digital I/O signals and control signals are TTL level signals.

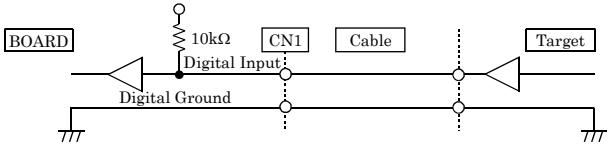


Figure 3.9. Digital Input Connection

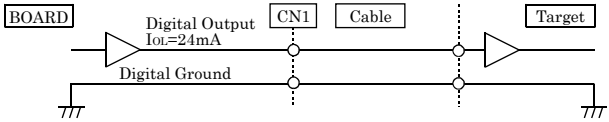


Figure 3.10. Digital Output Connection

Counter input signal control

The counter gate control input (see Connector Pin Assignment in Chapter3) enables or disables the external clock input to the counter. You can use this function to control the external clock input to the counter. The external clock input to the counter is enabled when the input is "High" and disabled when the input is "Low". As the pin has an internal pull-up on the board (or card), the default if not connected is "High". As a result, the external clock for the counter is enabled if this pin is not connected.

⚠ CAUTION

Do not short the output signals to analog ground, digital ground, and/or power line. Doing so may damage the board.

Reference

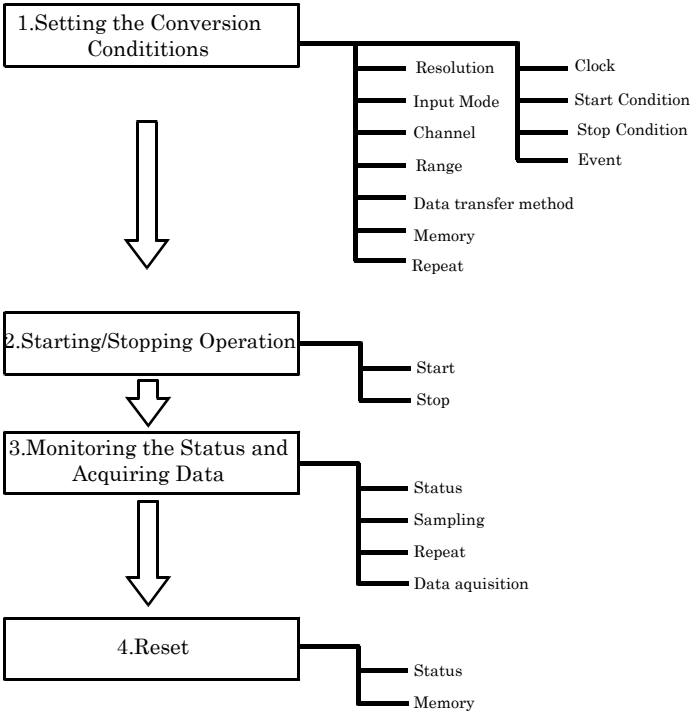
For the operation timings for control signal input, see “Control Signal Timings” in Chapter 6 “Hardware”.

4. Functions

This chapter describes the different functions that can be implemented using the hardware and driver together. Unless stated otherwise, the driver is assumed to be API-AIO(WDM).

Analog Input Function

The board converts analog signals to digital data according to the resolution and stores it in memory. You can set a variety of conditions for analog input, including the input channel, sampling period, and sampling start/stop conditions.
Analog input processes are classified as follows:



1. Setting the Conversion Conditions

First, set the conditions for executing analog input.

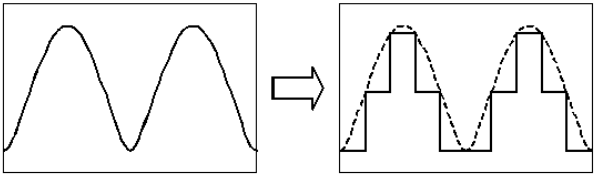
Resolution

"Resolution" signifies the number of bits used by an analog input device to represent analog signals. The higher the resolution, the more finely the voltage range is segmented, allowing the device to convert analog values to digital equivalents more precisely.

A device with a resolution of 12-bit divides the range width into 4096 segments.

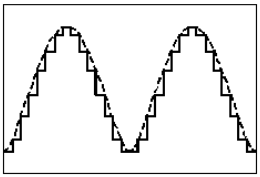
When the device covers the range of 0 - 10V, the minimum unit of converted voltages is 10÷4096 ≈ 2.44mV.

If the device has a resolution of 16-bit, it is 10÷65536 ≈ 0.153mV instead.



Analog Voltage before Conversion

Low Resolution Board



High Resolution Board

AD16-64(LPCI)LA : The resolution is 16-bit.

Input Mode

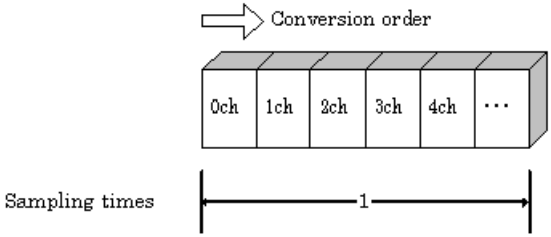
"Input Mode" indicates the method of connecting analog input signals.
The input modes available are single-ended input and differential input.
The single-ended input mode is suitable for the environment in which the potential difference between the signal source and ground and noise components can be ignored. For the environment in which they cannot be ignored, the differential input mode is suitable.
The number of channels available in differential input mode is half that in single-ended input mode.
This board uses on-board jumpers to set the input mode.

Channel

"Channel" represents each point of analog input.
For individual channel numbers, see "Using the On-board Connectors" to "Connector Pin Assignment" in Chapter 3 "External Connection".
You can specify an arbitrary number of points of analog input by setting the channels by means of software.

Channel conversion order

Normally, when performing conversion for more than one channel at each sampling, conversion is performed consecutively starting from channel 0.
Software setup is not required as this board uses a fixed channel conversion priority.



Range

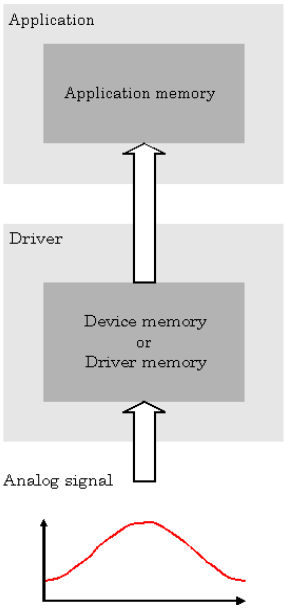
"Range" means the range of voltages at which analog input can be performed
Software setup of the range is not required as this board uses a fixed range of voltages.

AD16-64(LPCI)LA : ±10V

Data transfer method

A device buffer mode is available, which uses the device's or driver's conversion data storage memory.

- **Device buffer mode**
  When conversion starts, data is saved in the device buffer (memory on the device itself or in the driver).
  The device buffer can operate as FIFO or ring memory.
  The application calls an API function at an appropriate timing and fetches the conversion data from the device buffer.
  The device buffer mode provides function that allows the number of items of conversion data using the number of sampling times as a unit to obtain the number of items of conversion data directly from the voltage.



Device buffer mode

Memory format

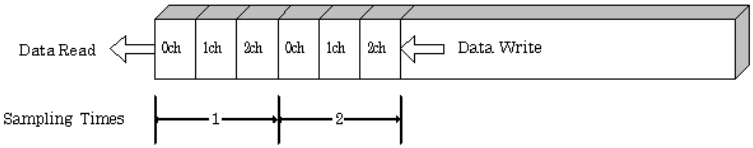
This board uses device buffer transfer mode; it does not require software setup of the memory format.

Device buffer mode

- FIFO format

  In the FIFO (First In First Out) format, input data items are read from memory in the same order in which they were written to the memory. Input data items are fed out of the memory sequentially, where the oldest one is always read from the memory. The status monitor and application notification functions are provided, which check and report the state in which the memory has stored a fixed amount of data or in which the memory has become full.

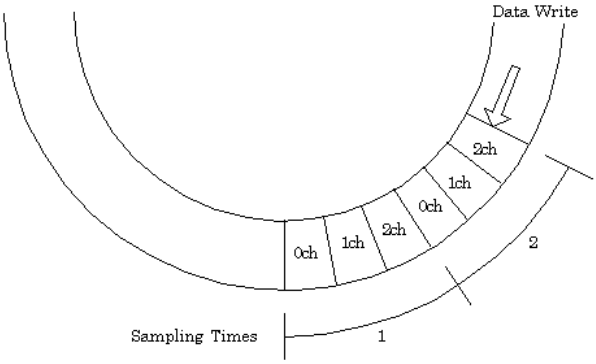
  The FIFO memory is used to obtain all input data from analog input in a short or infinite period of time.



- Ring format

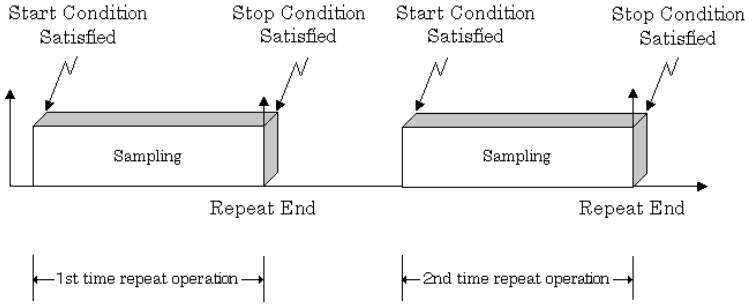
  In the ring format, the memory contains storage areas arranged in a ring. Input data items are written to the memory sequentially. When it stores data exceeding the limit, it overwrites the area storing the previous item of input data. The status monitor and application notification functions are provided, which check and report the state in which data has been written to certain areas of memory.

  The ring memory is used to obtain data where conversion has stopped due to some event, usually without obtaining data in the normal state.



Repeat

"Repeat" indicates the number of repetitions of sampling to be executed, from when the sampling start condition is satisfied until the end of sampling, including delayed sampling.
The number of repetitions is set by means of software, for which conversion is repeated. You can set an infinite number of repetitions, in which case the conversion is terminated by the software abort command.
Input data items are stored to the memory sequentially. The repetition state can be subject to status monitoring and application notification.



Clock

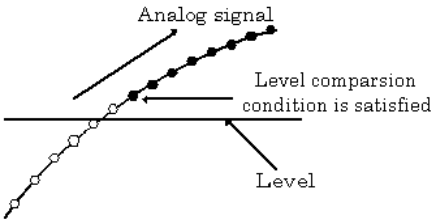
The sampling clock controls the sampling frequency. You can select both the internal sampling clock and external sampling clock.

- Internal sampling clock
  The clock signal from the on-board clock generator is used.
- External sampling clock
  The edge of the digital signal input from an external device is used for the sampling clock.

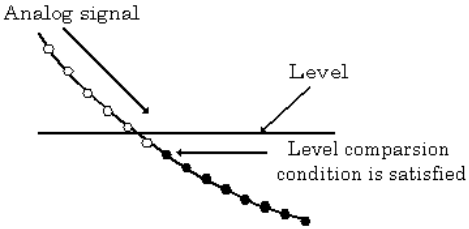
Start Condition

The condition for controlling the start of sampling can be selected from among software, input data comparison and an external trigger. The conditions for controlling the start and stop of sampling are completely independent of each other; they can be set separately.

- Software
  The board starts sampling and storing input data to memory immediately after the operation start command is issued.
- Input data comparison
  When the operation start command is issued, the board compares the analog signal input through a specified channel to the value of the preset comparison level. If the analog signal satisfies the condition, the board starts storing input data.
  Level comparison conditions are set as two conditions: level and direction.



The above sketch shows that the level comparison condition is satisfied in the rising direction. The start condition is satisfied when the analog signal at the specified channel passes the comparison level in the rising direction. Input data items are stored to memory, starting with those at solid dots.



The above sketch shows that the level comparison condition is satisfied in the falling direction. The start condition is satisfied when the analog signal at the specified channel passes the comparison level in the falling direction. Input data items are stored to memory, starting with those at solid dots.

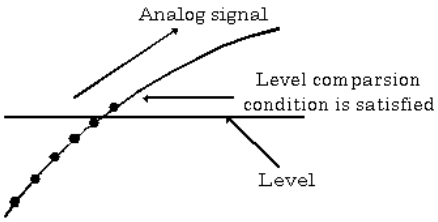
If you set the level comparison directions to both directions, the start condition is satisfied when the analog signal passes the level both in the rising and falling directions.

- External trigger
  The board starts waiting for an external control signal as soon as the operation start command is output.
  Sampling and data transfer to memory start when the specified edge (rising edge or falling edge) is input from the external control signal.

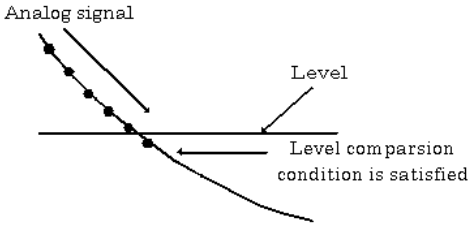
Stop Condition

The condition for controlling the stop of sampling can be selected from among the last sampling count, input data comparison, an external trigger, and software abort.
The board stops sampling whenever an error occurs irrespective of the stop condition setting.

- Last sampling count
  The board stops sampling after storing input data to memory for the specified number of times of sampling.
- Input data comparison
  Once the board has started sampling, it compares the analog signal input through a specified channel to the value of the preset comparison level. If the analog signal satisfies the condition, the board stops sampling.
  Level comparison conditions are set as two conditions: level and direction.



The above sketch shows that the level comparison condition is satisfied in the rising direction.
The stop condition is satisfied when the analog signal at the specified channel passes the comparison level in the rising direction. Input data items are stored to memory, ending until those at solid dots.

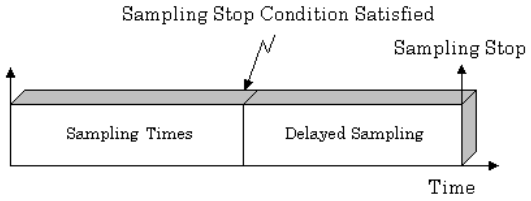


The above sketch shows that the level comparison condition is satisfied in the falling direction.
The stop condition is satisfied when the analog signal at the specified channel passes the comparison level in the falling direction. Input data items are stored to memory, ending until those at solid dots
If you set the level comparison directions to both directions, the start condition is satisfied when the analog signal passes the level both in the rising and falling directions.

- External trigger
  The board starts waiting for an external control signal as soon as the operation start command is output.
  Sampling and data transfer to memory start when the specified edge (rising edge or falling edge) is input from the external control signal.

Delay

Delayed sampling is performed after the sampling stop condition is satisfied.
When a sampling stop condition other than the software abort command is satisfied, the board performs sampling for the specified number of times of delayed sampling to store input data to memory.
If you set the number of times of delayed sampling to 0, the board stops sampling the moment the sampling stop condition is satisfied.



Event

"Event" works as a function for reporting the occurrence of a certain board state to the application.

The following events can be used in combination depending on the specifications and purpose of the application.

- "AD conversion start condition satisfied" event
  This event occurs when the AD conversion start condition is satisfied. The event is nullified when the conversion start condition is "software".
- "Repeat end" event
  This even occurs whenever a repetition is completed.
- "End of device operation" event
  This event occurs when the entire operation including repetitions is completed.
- "Stored specified sampling times" event
  This event occurs when sampling has been performed for the number of times set by software.
  This event can only be used in device buffer mode.
- Overflow event
  This event occurs at an attempt to store input data with the memory full.
- Sampling clock error event
  This event occurs when conversion stops as an error occurs due to a sampling clock period that is too short.
- AD conversion error event
  This event occurs when conversion stops due to an AD conversion error.

2. Starting/Stopping Operation

Sampling is started by the software command.
Once started, sampling can be stopped by the software command at any timing.

3. Monitoring the Status and Acquiring Data

Software commands are used to monitor the operation status of the device and to acquire input data from memory. Status monitoring and data acquisition can be performed even during sampling.

Status

The current state of the device can be checked by obtaining the device status.
The following types of device status are available:

- Device operating
  The “device operating” status remains ON, after the execution of the sampling start command until the board completes conversion, aborts operation due to an error, or stops sampling in response to the command.
- Waiting for start trigger
  This status remains ON, after the board starts sampling until the start trigger is input, if the conversion start condition is an external trigger or level comparison. The status is set to OFF when the input trigger is input to start conversion.
  The status is set to ON whenever the board enters the conversion start wait status even when repeated operation has been set.
- Specified sampling data stored
  This status is set to ON when input data stored in memory has reached the amount corresponding to the preset number of times of sampling.
  If the memory format is FIFO, the status is set to OFF when the amount of input data in the memory falls below the value corresponding to the preset number of times of sampling as data is acquired.
  Once the status is set to ON when the memory format is ring, it remains ON until it is reset.
- Overflow
  An overflow error occurs when an attempt is made to store input data to memory while it has been full of input data.
  When the memory format is FIFO, the board stops conversion.
  When the memory format is ring, the board continues conversion while overwriting existing data with new one.
- Sampling clock error
  This error occurs when the sampling clock period is too short.
- AD conversion error
  If the “device operating” status remains ON (without terminating conversion) for an extended period of time, the driver regards that state as an operation error and sets this status to ON. This error stops sampling.

Sampling

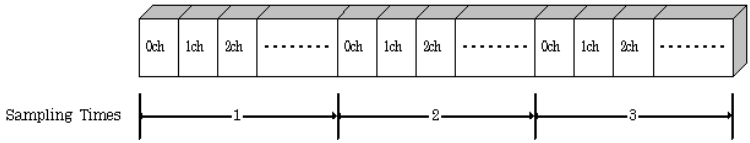
The number of sampled items of input data stored in memory can be obtained by the software command.

Repeat

The current repeat count can be obtained by the software command.

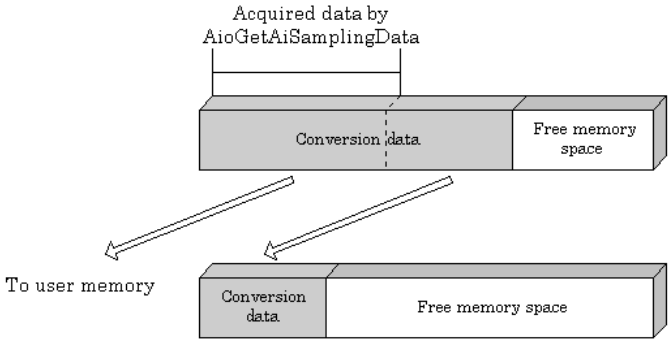
Data acquisition

The conversion data stored in memory can be retrieved using a software command.
The figure below shows the correspondence between the sampling count and the conversion channel for the conversion data stored in memory.

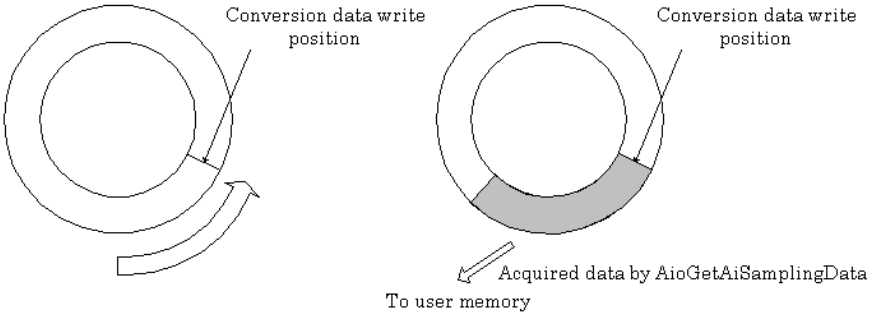


Input data is acquired differently depending on the memory format used.

- Data acquisition in FIFO format
  When FIFO memory is used, the oldest data is always read first.
  The following sketch shows an image of data acquisition in FIFO format.
  When data is acquired from the memory, the free memory space increases by that data size. When data is acquired next, the oldest one of the existing data items is taken from the memory in the same way.
  The FIFO memory deletes data once that data is acquired.



- Data acquisition in ring format
  When ring memory is used, data is read always with respect to the current input data write position.
  The following sketch shows an image of data acquisition in ring format.
  The sampling count obtained is always the number of times of sampling for up to the latest data (shaded portion below).
  The larger the number of samples taken, the older the data item acquired first.
  As the ring memory retains data even after that data is acquired, you can fetch the same data any number of times.



Conversion data

The following equation represents the relationship between input data and voltage.

Voltage = Input data x (Max. range value - Min. range value) / Resolution + Min. range value

The value of resolution for the 16-bit device is 65536.
The table below shows the relationship between input data and voltage in the ±10-V range.

Voltage	Conversion data (16-bit)
+9.99970V	65535
:	:
0.00030V	32769
0V	32768
-0.00030V	32767
:	:
-10.000V	0

Ex.: When input data 49152 is input at a resolution of 16-bit in the ± 10-V range
Voltage = 49152 x (10 - (-10)) ÷65536 + (-10)
= 5.0

4.Reset

Various states can be reset by executing the following reset commands:

Status

This command resets the sampling clock error status and AD conversion error status.

Memory

This can only be used when the transfer mode is set to device buffer mode.

This command resets the following memory related states.

- Resets the conversion data in memory.
- Resets the repeat count to 0.
- Resets the sampling count to 0 when a stop trigger is input.
- Resets the buffer overflow status.
- Resets the status information for the specified data save count.

Counter Function

1. Setting the Operating Conditions

This specifies the conditions for counter operation.

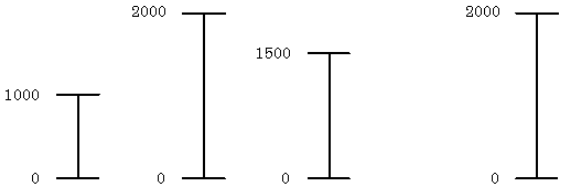
Operating conditions

The basic operation of the counter is to count an external input signal.
The counter includes a function to detect a count match and perform a specified operation when the current count value reaches a preset count value.

Compare count values

The compare count load function automatically loads the next compare count value when a count match occurs.

Time of count compare match	1st	2nd	3rd	. . . .	nth
Cnt[n]: Compare match value	Cnt[0] = 1000	Cnt[1] = 2000	Cnt[2] = 1500	. . . .	Cnt[n-1] = 2000



The figure above shows an example of using the compare count load function.

After the counter starts, the first count match occurs when the count reaches 1000.

When the count reaches 1000, the counter value at which the second compare count match is to occur (2000) is set.

This continues with the next value from the array being set each time a count match occurs.

After the final value from the array is loaded, operation can start again from the beginning of the array.

Alternatively, loading can be halted (in which case, the compare count value remains at 2000).

Input signal

The external clock can be selected as the counter input signal.

Digital filter

A digital filter can be used on external input bits.
The filter time can be set to "don't use", 1μs by software.

Event

The event function notifies the application when something occurs on the device.
The following events can be used as required.

- Compare count match event
  This event is triggered when a compare match occurs on the counter.
- Count overrun event
  This event is triggered when a counter overrun occurs.
- Counter operation error
  This event is triggered when a counter operation error causes the counter to stop.

2. Starting/Stopping Operation

Starting and stopping the counter are performed using software commands.
Once the counter has started, it can be stopped at any time by a software command.

3. Monitoring the Status and Acquiring Data

Software commands can be used to monitor the device operating status and read counter data. Status monitoring and data acquisition can both be performed while the counter is running.

Status

The current state of the device can be checked by obtaining the device status.
The following types of device status are available:

- Counter operating
  The device operating status is ON from the time the operation start command is executed until operation stops due to a stop command or error.
- Compare count match
  The compare count match status turns ON when a count match occurs after the counter is started. The status is turned OFF by the status reset command.
- Overrun
  The overrun status turns ON if another count match occurs when the compare count match status is already ON. The status is turned OFF by the status reset command.
  Even if the overrun status turns ON, this does not stop the counter.
- Counter operation error
  Execution of driver processing may not be able to keep up if multiple count match events occur within a short time period.
  In this case, the counter operation error status turns ON and counter operation stops.

Data acquisition

The current count value can be read using a software command.

4.Reset

Various states can be reset by executing the following reset commands:

Counter reset

Resets the counter. This restores the counter to its state after power on.

Status

Resets the compare count match status and overrun status.

Digital Input Function

Input bit

Individual digital input points are called input bits.
When the number of input points of a device is 4, the bits are determined as bit 0 - bit 3.

Bit 3	Bit 2	Bit 1	Bit 0

Input in Bits

The state 1 (ON) or 0 (OFF) of each input bit can be obtained by specifying the bit.

Input in Bytes

Individual input bits can be input in byte units.

When the number of input points of the device is 4, the individual input bits are arranged as shown below and the byte data to be input is a value between 0 and 15 depending on the states of the bits.

EX. Input of bit 3 (OFF), bit 2 (ON), bit 1 (OFF), bit 0 (ON)
Byte data = 05(5H)

Bit 3	Bit 2	Bit 1	Bit 0
0(OFF)	1(ON)	0(OFF)	1(ON)

Digital filter

A digital filter can be used on the input bits.
The filter time can be set to "don't use", 1μs by software.

Digital Output Function

Output bit

Individual digital output points are called output bits.
When the number of output points of a device is 4, the bits are determined as bit 0 - bit 3.

Bit 3	Bit 2	Bit 1	Bit 0

Output in Bits

The state of each output bit can be changed to ON or OFF by specifying the bit and setting it to 1 or 0.

Output in Bytes

Individual output bits can be output in byte units.
When the number of output points of the device is 4, the individual output bits are arranged as shown below and byte data to be output is a value between 0 and 15.

Ex. Output of bit 3 (ON), bit 2(OFF), bit 1 (ON), bit 0 (OFF)
Byte data = 10(AH)

Bit 3	Bit 2	Bit 1	Bit 0
1(ON)	0(OFF)	1(ON)	0(OFF)

5. About Software

Bundled disk Directory Structure

```
\
├─ Autorun.exe          Installer Main Window
|  Readmej.html         Version information on each driver (Japanese)
|  Readmeu.html         Version information on each driver (English)
  .
  .
├──APIPAC               Each installer
|      ├─AIO
|      |    ├─DISK1
|      |    ├─DISK2
|      |    ├─……
|      |    ├─DISKN
|      ├─AioWdm
|      ├─CNT
|      ├─DIO
|      ├─……
  .
  .
|─HELP                  HELP file
|      ├─Aio
|      ├─Cnt
|      ├─……
  .
  .
|─INF                   Each INF file for OS
|      ├─WDM
|      ├─Win2000
|      ├─Win95
  .
  .
├─linux                 Linux driver file
|      ├─cnt
|      ├─dio
|      ├─……
  .
  .
|─Readme                Readme file for each driver
  .
  .
|─Release               Driver file on each API-TOOL
|      ├─API_NT         (For creation of a user-specific install program)
|      ├─API_W95
  .
  .
|─UsersGuide            Hardware User's Guide(PDF files)
```

About Software for Windows

The bundled disk “Driver library API-PAC(W32)” contains the functions that provide the following features:

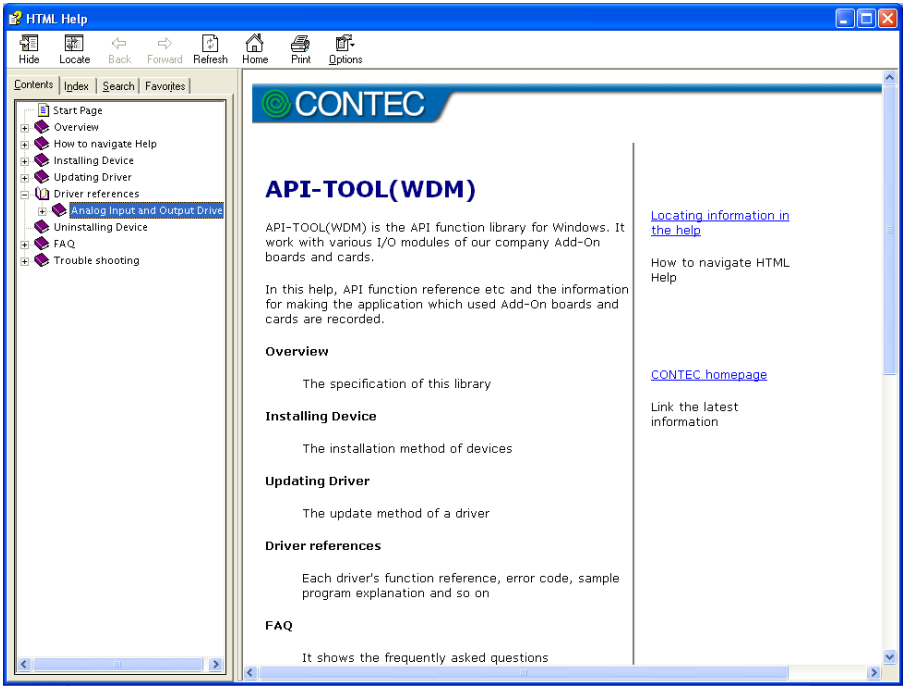
- Analog input or output through arbitrary channels
- Analog input at arbitrary intervals using the internal or external sampling clock
- Simultaneous monitoring of the termination of analog input sampling, buffer memory usage, and interrupt events such as occurrences of errors
- Driver option check using a demo driver even without the board installed

For details, refer to the help file. The help file provides various items of information such as “Function Reference”, “Sample Programs”, “Tutorial”, “FAQs”and “Troubleshooting”. Use them for program development and troubleshooting.

Accessing the Help File

(1) Click on the [Start] button on the Windows taskbar.

(2) From the Start Menu, select “Programs” – “CONTEC API-PAC(W32)” – “AIOWDM” – “API-AIO(WDM) HELP” to display help information.

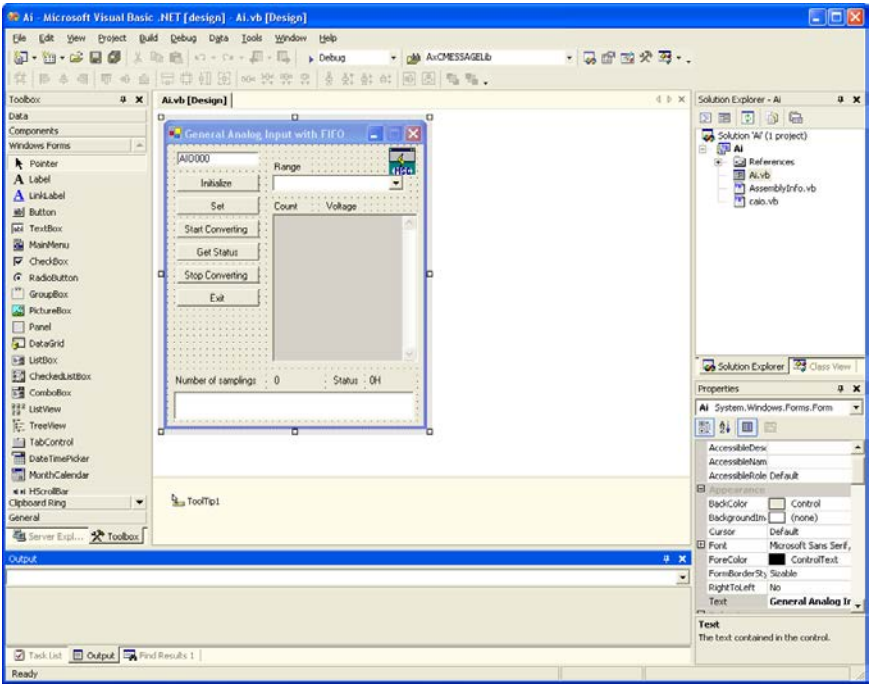


Using Sample Programs

Sample programs are provided for each of the basic operations. You can use these to check the operation of the board and as a reference when writing your own programs.

To use the sample programs, specify the device name in the property page for the program.

The sample programs are stored in \Program Files\CONTEC\API-PAC(W32)\AIOWDM\Samples.



Running a Sample Program

(1) Click on the [Start] button on the Windows taskbar.

(2) From the Start Menu, select “Programs” – “CONTEC API-PAC(W32)” – “AIOWDM” – “SAMPLE…”.

(3) A sample program is invoked.

Sample Programs - Examples

Analog input

Simple sample program

- SingleAi	Perform single analog input from specified channel
- MultiAi	Perform single analog input from multiple channels

Device buffer

- Ai	Perform standard analog input using a FIFO buffer
- AiPoll	Perform standard analog input by polling
- AiEx	Perform analog input for multiple channels using a FIFO buffer
- AiLong	Perform long-duration analog input using a FIFO buffer
- AiExt	Perform analog input using an external clock
- AiTrg	Perform analog input using an external trigger to start and stop operation
- AiLevel1	Use a level trigger to start analog input
- AiLevel2	Use a level trigger to stop analog input
- Ai2	Perform standard analog input using more than one device
- AiCall	Perform analog input using a callback routine

Analog output

Simple sample program

- SingleAo	Perform single analog output from specified channel
- MultiAo	Perform single analog output from multiple channels

Device buffer

- Ao	Perform standard analog output using a FIFO buffer
- AoPoll	Perform standard analog output by polling
- AoEx	Perform analog output for multiple channels using a FIFO buffer
- AoLong	Perform long-duration analog output using a FIFO buffer
- AoExt	Perform analog output using an external clock
- AoRing	Perform continuous analog output using a ring buffer
- AoTrg	Perform analog output using an external trigger to start and stop operation
- Ao2	Perform standard analog output using more than one device
- AoCall	Perform analog output using a callback routine

Digital I/O

- DioBit	Perform digital I/O using bit values
- DioByte	Perform digital I/O using byte values

Counter/Timer

- Counter	General purpose counter
- Interval	Interval timer
- Watch	Stopwatch timer

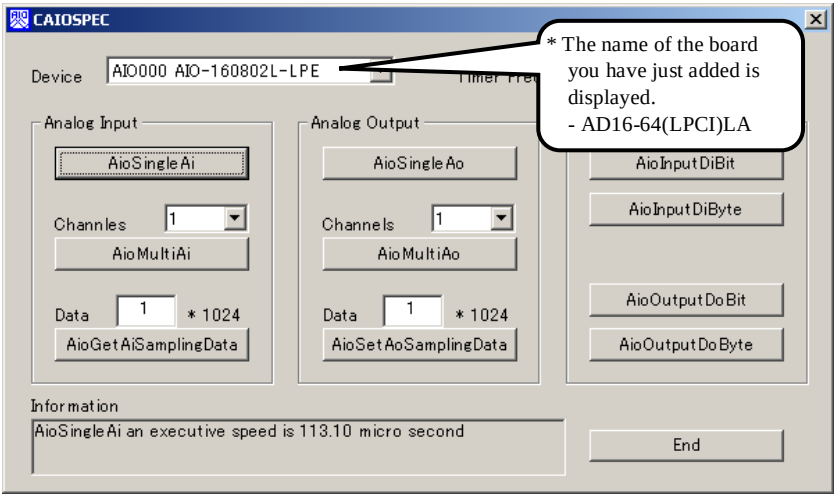
Others

- Convert	Data conversion

Usage of Utility Program

Program for Measurement of Function Execution Speed

The execution time of some main functions can be measured in a function execution speed measurement program. To use a function execution speed measurement program, click the [execution time measurement] button in the diagnostic program.



Procedure

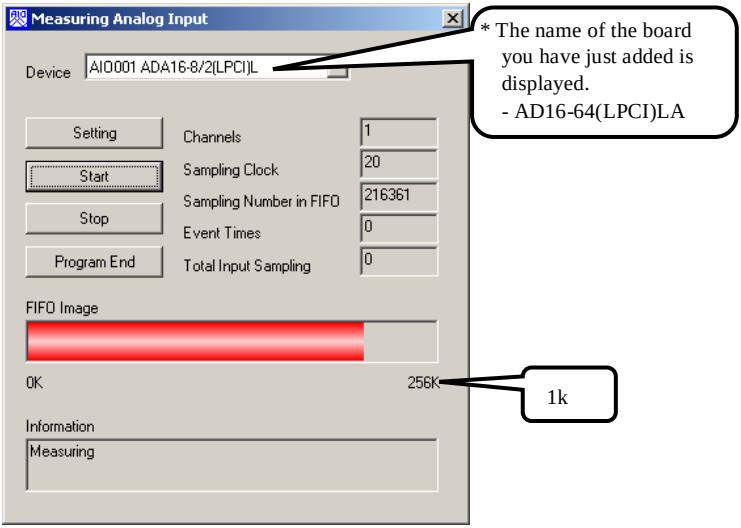
(1) Chose the measure device from device list.

(2) Click the button written with the function name to measure the execution speed of the function. Please choose from a list the number of channels used for conversion in function AioMultiAi and AioMultiAo. Input the transmission data size in function AioGetAiSamplingData and AioSetAoSamplingData. The transmission data is set by unit of kByte.

(3) End the application with an [end] button.

Analog Input Measurement Tool

It is an analog input measurement utility to carry out infinity sample in the FIFO memory. Once the conversion data of memory accumulates to a certain quantity, the event occurs and data of the memory is acquired. Data in the FIFO memory can be confirmed visually.

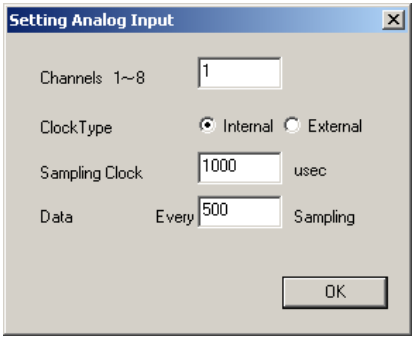


The number of channels used, the internal/external clock, the conversion speed, and the sampling frequency at which an event generates can be set. Since the notification of a sampling clock error event is sent, please make use of it for the conversion spec measurement under various conversion conditions.

Procedure

(1) Chose the device name of the device to be used from the upper left combo box, and click the setting button.

(2) The conversion conditions are set on the screen of the analog input setting.
Once an input is done at the sampling frequency specified as data taking-in sampling, an event occurs and data will be acquired. Click the OK button to finish setting the conditions, and returns to former screen.



(3) Start the measurement with measurement start button. The various states during the conversion are displayed.

The number of the samplings in FIFO is :
It is conversion data taken in the memory. This can be visually checked in a "memory image".

Event generation sampling frequency :
When the number of input sampling in FIFO reaches this frequency, the event generates.

Total input sampling frequency :
It is the total number of samplings for application in the memory.

Measurement may stop by the following errors.

Sampling clock error :
It means that the conversion speed is too fast and the driver processing is not in time when converting at the internal clock.
The cycle of the clock is too fast when converting it at the external clock. Moreover, the cause by noise etc. is also concerned.

Buffer overflow :
The memory overflows since the conversion speed is too fast compared with the one at which data is inputted.

(4) Click the “stop” button, and measurement stops.

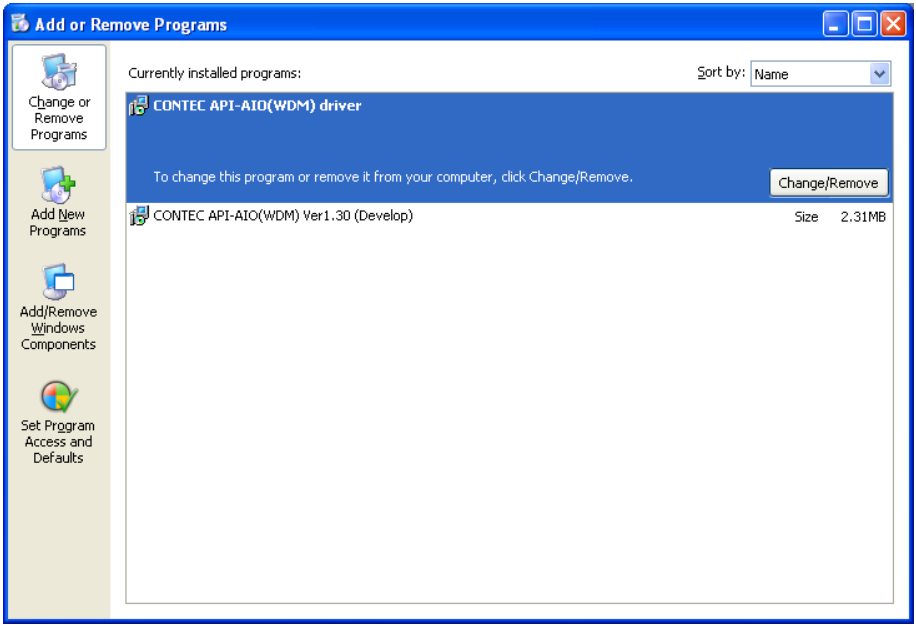
Uninstalling the Driver Libraries

To uninstall API-PAC(W32), follow the procedure below.

(1) Click on the [Start] button on the Windows taskbar. From the Start Menu, select “Settings” – “Control Panel”.

(2) Double-click on “Add or Remove Programs” in the Control Panel.

(3) Select "CONTEC API-AIO(WDM) driver" and "CONTEC API-AIO(WDM) VerX.XX (Development environment)" from the list of applications.
Click the [Change or Remove Programs] button. Proceed with uninstalling by following the instructions that appear on the screen.



About Software for Linux

The Linux version of digital I/O function driver, API-DIO(LNX), provides functions that execute the following features:

- Analog I/O of specified channels
- It is possible to operate as a set parameter to the analog I/O board is preserved by the default value, and the setting of the parameter doesn't exist.

For details, refer to the help file. The help file provides various items of information such as “Function Reference”, “Sample Programs”, and “FAQs”. Use them for program development and troubleshooting.

Driver Software Install Procedure

The Linux version for analog I/O driver, API-AIO(LNX), is supplied as a compressed file /linux/aio/caioXXX.tgz on the bundled disk. (Note: XXX represents the driver version.)

Mount the bundled disk as shown below, copy the file to an arbitrary directory, and decompress the file to install the driver.

For details on using the driver, refer to readme.txt and the help file in HTML format extracted by installation.

To install the driver, log in as a superuser.

Decompression and setup procedure

```
# cd
 # mount /dev/cdrom /mnt/cdrom                    Mount the bundled disk.
 # cp /mnt/cdrom/linux/aio/caioXXX.tgz ./         Copy the compressed file.
 # tar xvfz caioXXX.tgz                           Decompress the compressed
file.
 ................
 # cd contec/caio
 # make                                           Compile the file.
 ................
 # make install                                   Install.
 ................
 # cd config
 # ./config                                       Set up the board to be used.
..... Set as follows.........
 # ./contec_aio_start.sh                          Start the driver.
# cd
```

Accessing the Help File

(1) Invoke a web browser in your X-Window environment.

(2) In the browser, open apitool.htm in the contec/caio/help directory.

Using Sample Programs

Sample programs have been prepared for specific basic applications.
Sample programs for each language are contained in the contec/caio/samples directory. For compiling them, refer to the manual for the desired language.

Uninstalling the driver

To uninstall the driver, use the uninstall shell script contained in the contec/caio directory. For details, check the contents of the script.

6. About Hardware

This chapter provides hardware specifications and hardware-related supplementary information.

For detailed technical information

For further detailed technical information ("Technical Reference" including the information such as an I/O map, configuration register, etc.), visit the Contec's web site (http://www.contec.com/support/) to call for it.

Hardware specification

Table 6.1. Specification ***< 1 / 2 >***

Item		Specification
Analog input		
	Isolated specification	Un-Isolated
	Input type	Single-Ended Input or Differential Input (by software)
	Input channel	64ch
	Input range	Bipolar ±10V
	Absolute max. input voltage	±20V
	Input impedance	1MΩ or more
	Resolution	16Bit
	Non-Linearity error *1 *2	±5LSB
	Conversion speed	10μ sec/ch
	Buffer memory	1k Word
	Conversion start trigger	Software / external trigger
	Conversion stop trigger	Number of sampling times / external trigger / software
	External start signal	TTL level (Rising or falling edge can be selected by software) Digital filter (select 1μ sec by software)
	External stop signal	TTL level (Rising or falling edge can be selected by software) Digital filter (select 1μ sec by software)
	External clock signal	TTL level (Rising or falling edge can be selected by software)
Digital I/O		
	Number of input channels	Un-Isolated input 4ch (LVTTL level positive logic)
	Number of output channels	Un-Isolated output 4ch (LVTTL level positive logic)
Counter		
	Number of channels	1ch
	Counting system	Up count
	Max. count	FFFFFFFFh (Binary data,32bit)
	Number of external inputs	2 TTL level (Gate/Up) Gate (High level), Up (Rising edge)
	Number of external outputs	TTL level Count match output (positive logic, pulse output)
	Response frequency	10MHz (Max.)

Table 6.1. Specification *< 2 / 2 >*

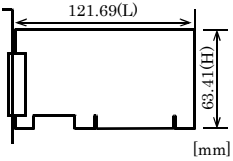
Item		Specification
Common		
	I/O address	64 ports boundary
	Interruption level	Errors and various factors, One interrupt request line as INTA
	Connector	HDRA-E68W1LFDT-SL [HONDA] or equivalent to it
	Power consumption	5VDC 450mA (Max.)
	Operating condition	0 - 50°C, 10 - 90%RH (No condensation)
	PCI bus specification	32bit, 33MHz, Universal key shapes supported *3
	Dimension (mm)	121.69(L) x 63.41 (H)
	Weight	60g

*1: The non-linearity error means an error of approximately 0.1% occurs over the maximum range at 0°C and 50°C ambient temperature.

*2: At the time of the source use of a signal which built in the high-speed operational amplifier.

*3: This board requires +5V power supply from expansion slots (it does not operate in the environment of only +3.3V power supply).

Board Dimensions



The standard outside dimension(L) is the distance from the end of the board to the outer surface of the slot cover.

Block Diagram

Figure 6.1 is a circuit block diagram of this board.

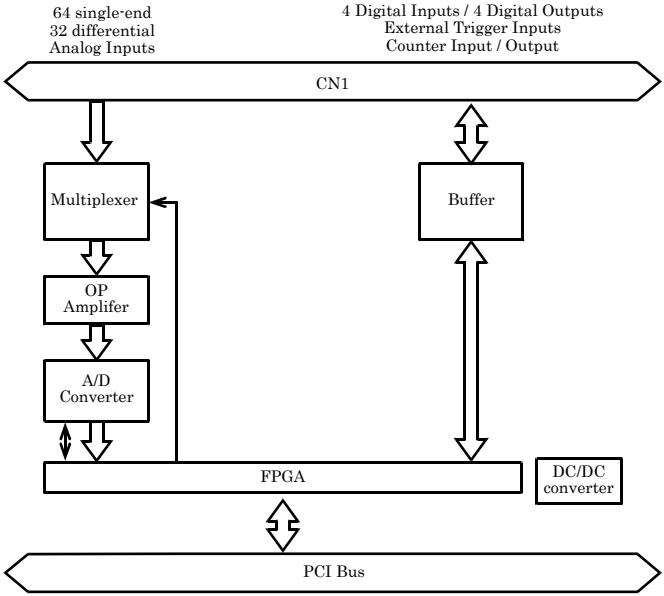


Figure 6.1. Block Diagram

Control Signal Timings

Control Signal Timings for Analog Input

Figures 6.2, 6.3, 6.4, and Table 6.2 show the control signal timings for the analog input function.

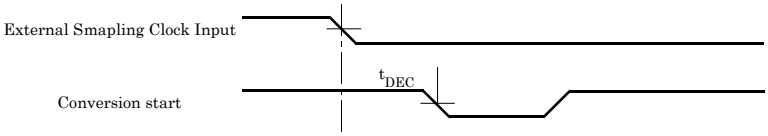


Figure 6.2. Timing Chart of External Sampling Clock

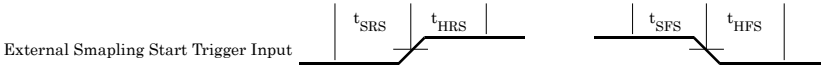


Figure 6.3. Timing Chart of Sampling Start Control Signal

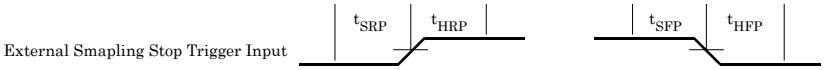


Figure 6.4. Timing Chart of Sampling Stop Control Signal

Table 6.2. Control Signal Timings

Parameter	Symbol	Time	Unit
Delay time from external sampling clock to first A/D start pulse	tDEC	100	nsec
Set up time of sampling start (Rising edge)	tSRS	100	nsec
Hold time of sampling start (Rising edge)	tHRS	100	nsec
Set up time of sampling start (Falling edge)	tSFS	100	nsec
Hold time of sampling start (Falling edge)	tHFS	100	nsec
Set up time of sampling stop (Rising edge)	tSRP	100	nsec
Hold time of sampling stop (Rising edge)	tHRP	100	nsec
Set up time of sampling stop (Falling edge)	tSFP	100	nsec
Hold time of sampling stop (Falling edge)	tHFP	100	nsec

⚠ CAUTION

The times listed in Table 6.2 are for standard operating conditions.

Control Signal Timings for Analog Output

Figures 6.5, 6.6 and Table 6.3 show the control signal timings for the analog input function.

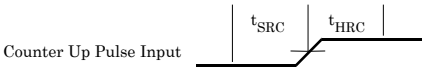


Figure 6.5. Timing Chart of Counter Input Signal

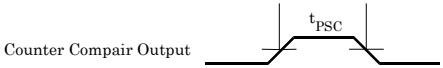


Figure 6.6. Timing Chart of Counter Output Signal (Pulse output)

Table 6.3. Control Signal Timings

Parameter	Symbol	Time	Unit
Set up time of counter input (Rising edge)	tSRC	100	nsec
Hold time of sampling start (Rising edge)	tHRC	100	nsec
Pulse width of counter output signal	tPSC	1000	nsec

⚠ CAUTION

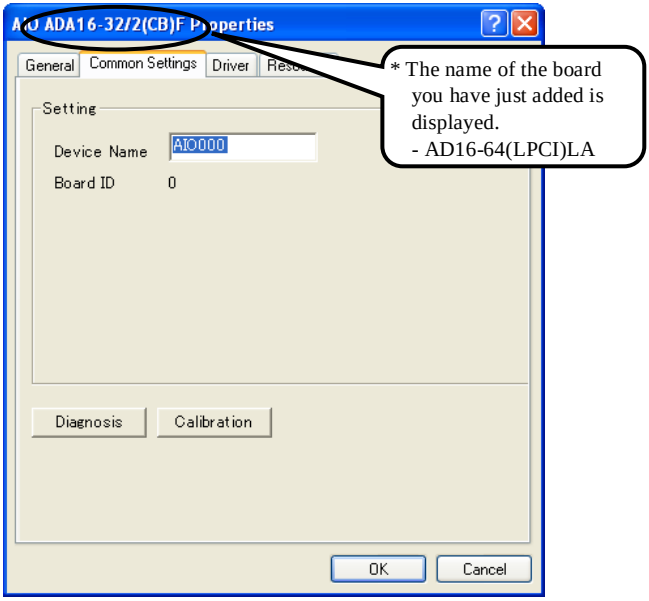
The times listed in Table 6.3 are for standard operating conditions.

About Calibration

Although this board is calibrated before shipping, you can use the calibration program to calibrate analog input yourself.

Starting the calibration program

Click the [Calibration] button on the property page for the device to start the calibration program.



Proceed with connecting the calibration equipment and performing the calibration in accordance with the instructions displayed by the calibration program.

Analog input calibration

Analog input calibration requires a reference voltage generator.
As the analog input has 16-bit resolution, use a reference voltage generator with a precision of at least 5 digits after the decimal point.
Calibrate one channel only for each range that you use.

Factory setting

You can use the calibration program to restore the factory calibration settings.

AD16-64(LPCI)LA

User's Guide

CONTEC CO., LTD.

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